

Hard Real Time Design

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Abstract

Hard Real Time design is the fundament of system performance. We will use a television as case to show hard real time design strategies, starting with a straightforward analog television based on a synchronous design and incrementally extending the television to become a full-fledged digital TV with many concurrent functions.

The complete course ASPTM is owned by Embedded Systems Institute. To teach this course a license from Embedded Systems Institute is required. This material is preliminary course material. The final material and course information can be found at: www.esi.nl/cursus.

All Gaudi documents are available at:
<http://www.gaudisite.nl/>

version: 0.2

status: planned

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1 Acknowledgements

The diagrams are a joined effort of Roland Mathijssen, Teun Hendriks and Gerrit Muller. Most of the material is based on material from the EXARCH course created by Ton Kostelijk.

References

- [1] Gerrit Muller. The system architecture homepage. <http://www.gaudisite.nl/index.html>, 1999.

History

Version: 0.2, date: 12 February, 2007 changed by: Gerrit Muller

- moved lot of material as part of refactoring
- changed status into planned

Version: 0.1, date: June 13, 2006 changed by: Gerrit Muller

- layout and reorder

Version: 0, date: December 16, 2005 changed by: Gerrit Muller

- Created, no changelog yet