

Status of IT Architecting: Progression or Regression?

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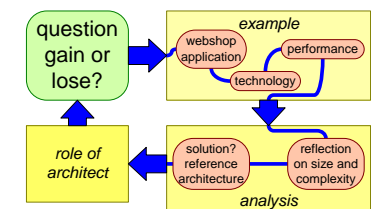
Abstract

Today's IT capabilities are seemingly limitless. From the point of view of last century we have amazing functionality available to consumers, businesses, governments et cetera. Technology advances have made this possible. At the same time we suffer from unwanted, unexpected incidents, ranging from slow or no response to loss or theft of sensitive data. The growth of systems and its complexity play a role. We will look at the role of the human creators of these systems and the available technology to discuss our concurrent progression and regression, and we will look at the role of the architect in particular.

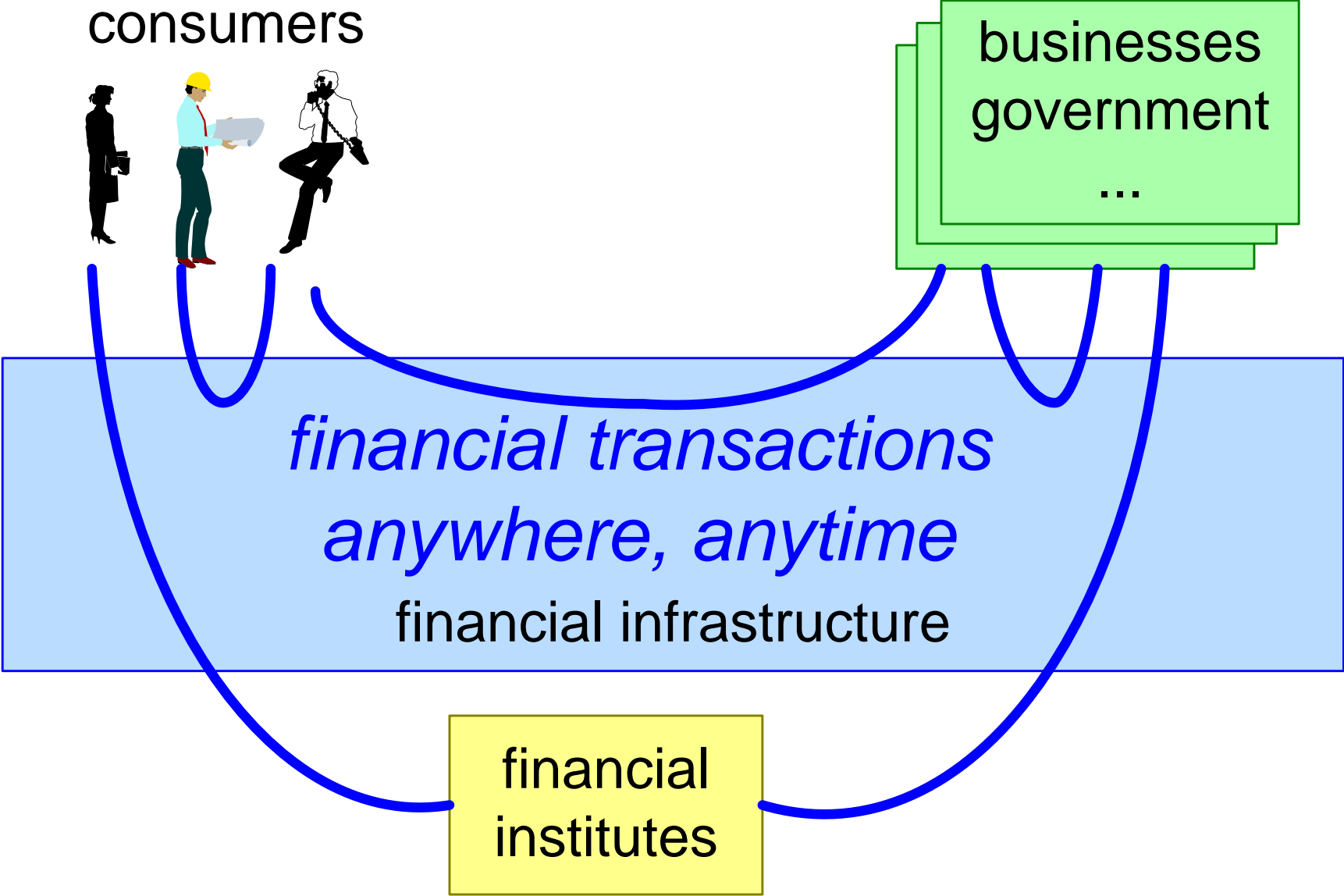
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August 16, 2025
status: preliminary
draft
version: 0

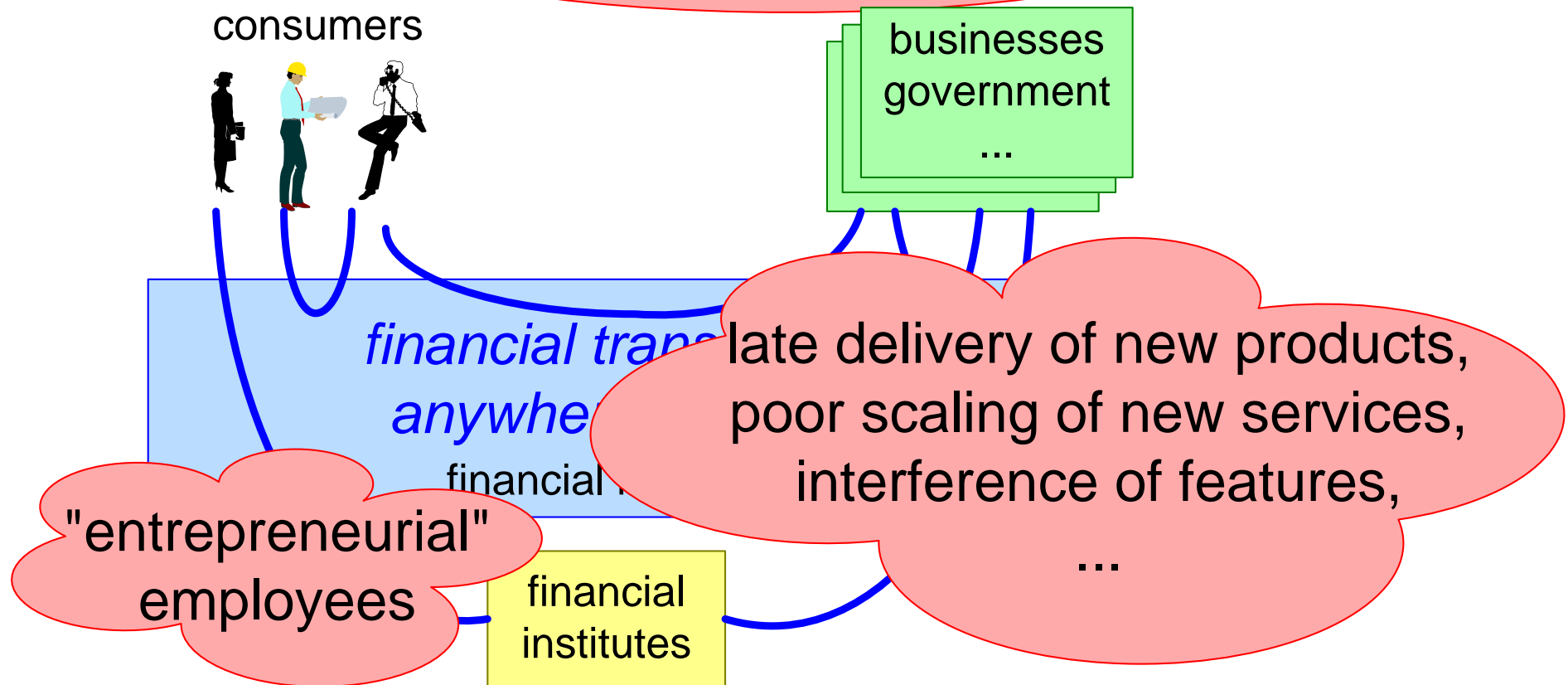


Functionality is Limitless



But Problems seem to be Pervasive

slow response, outages, human-less helpdesks, silly excuses (the computer could not...), identity-theft, lost privacy



Do we Gain or do we Lose?

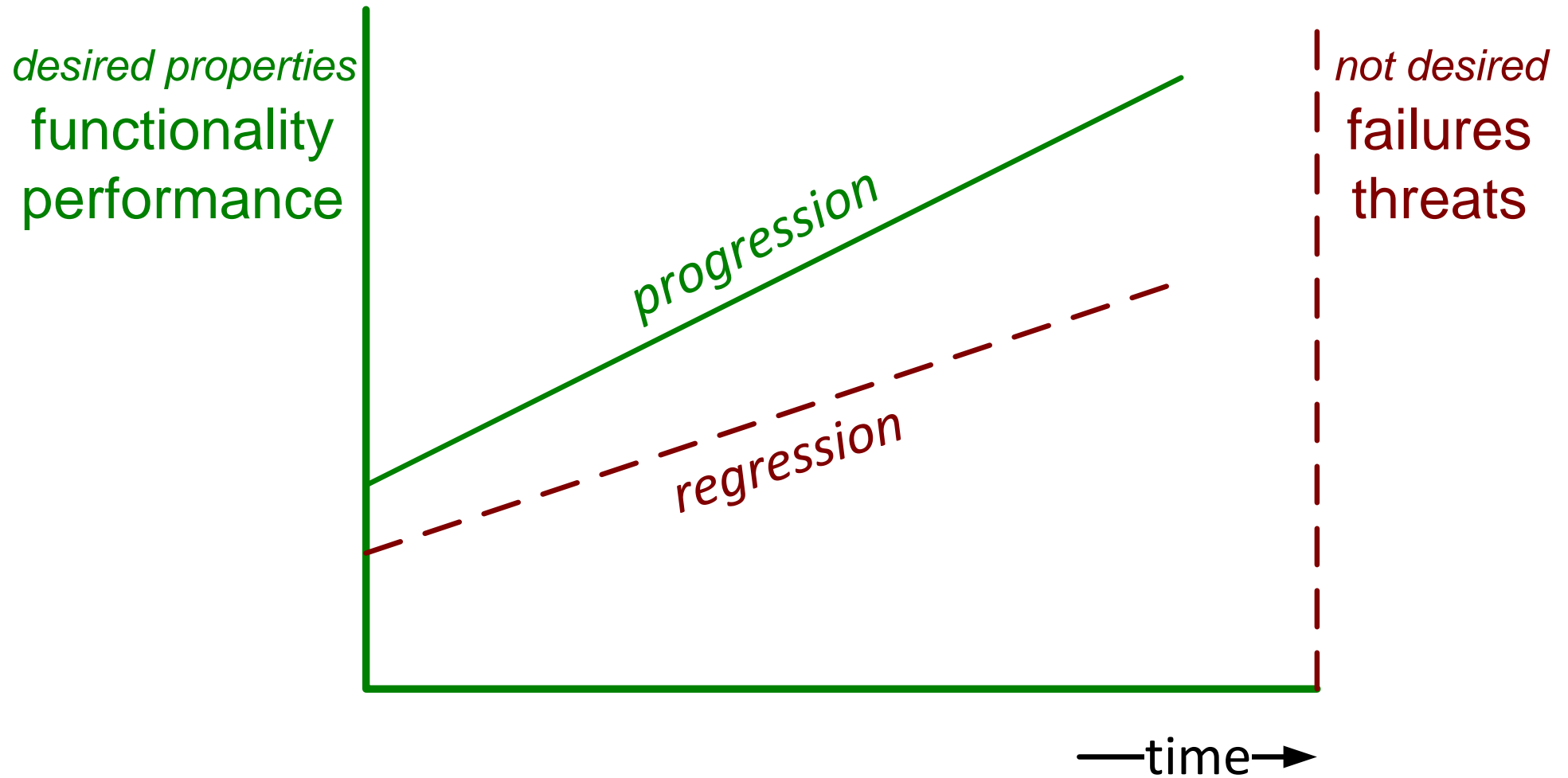
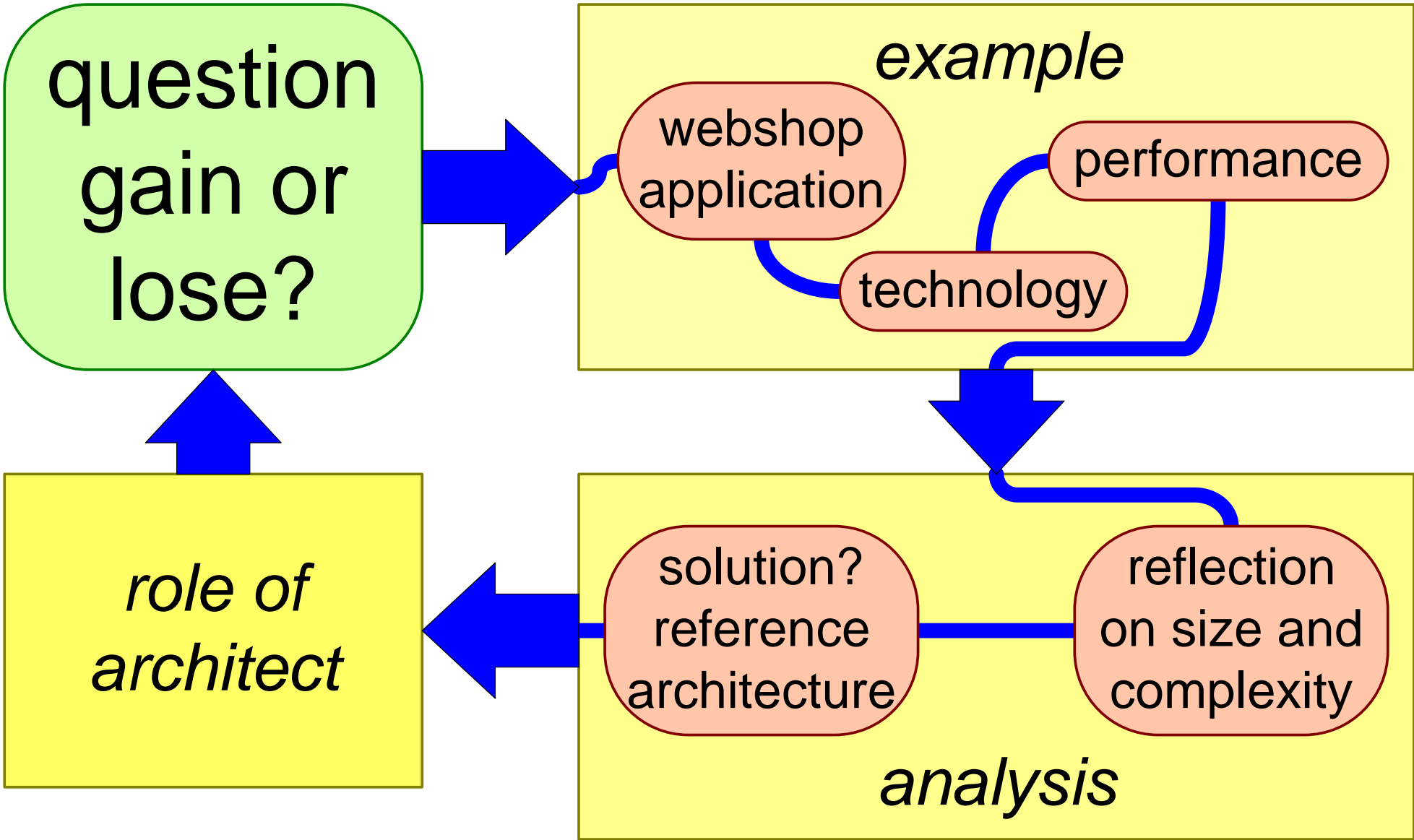


Figure Of Contents™



Example, Case Webshop

main access through search

personalization

catalogue entries

Up-to-date information:
Bestsellers
What Other Customers Are Looking At Right Now

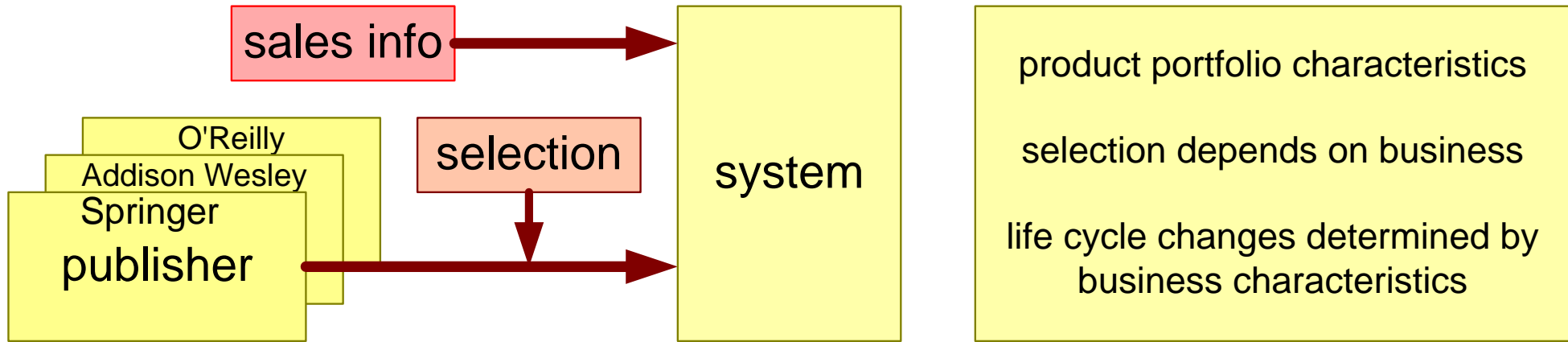
other advertisements

styling: frequently updated, fashion!

standard boilerplate

snapshot of
www.amazon.com

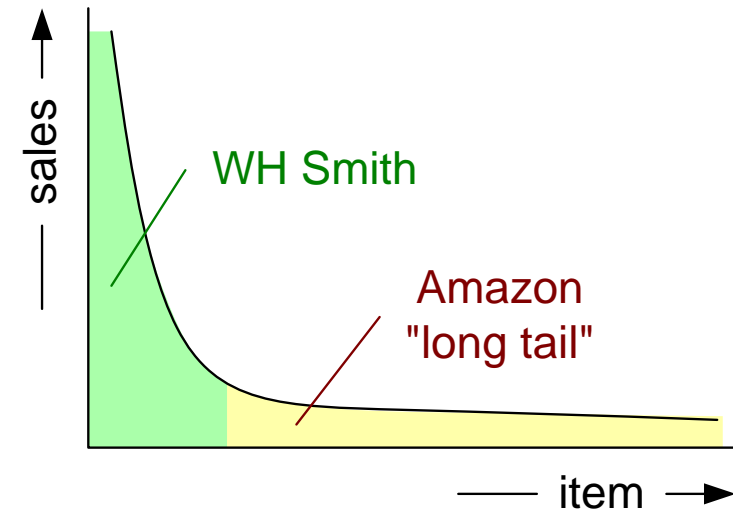
Some Numbers: New Books per Year



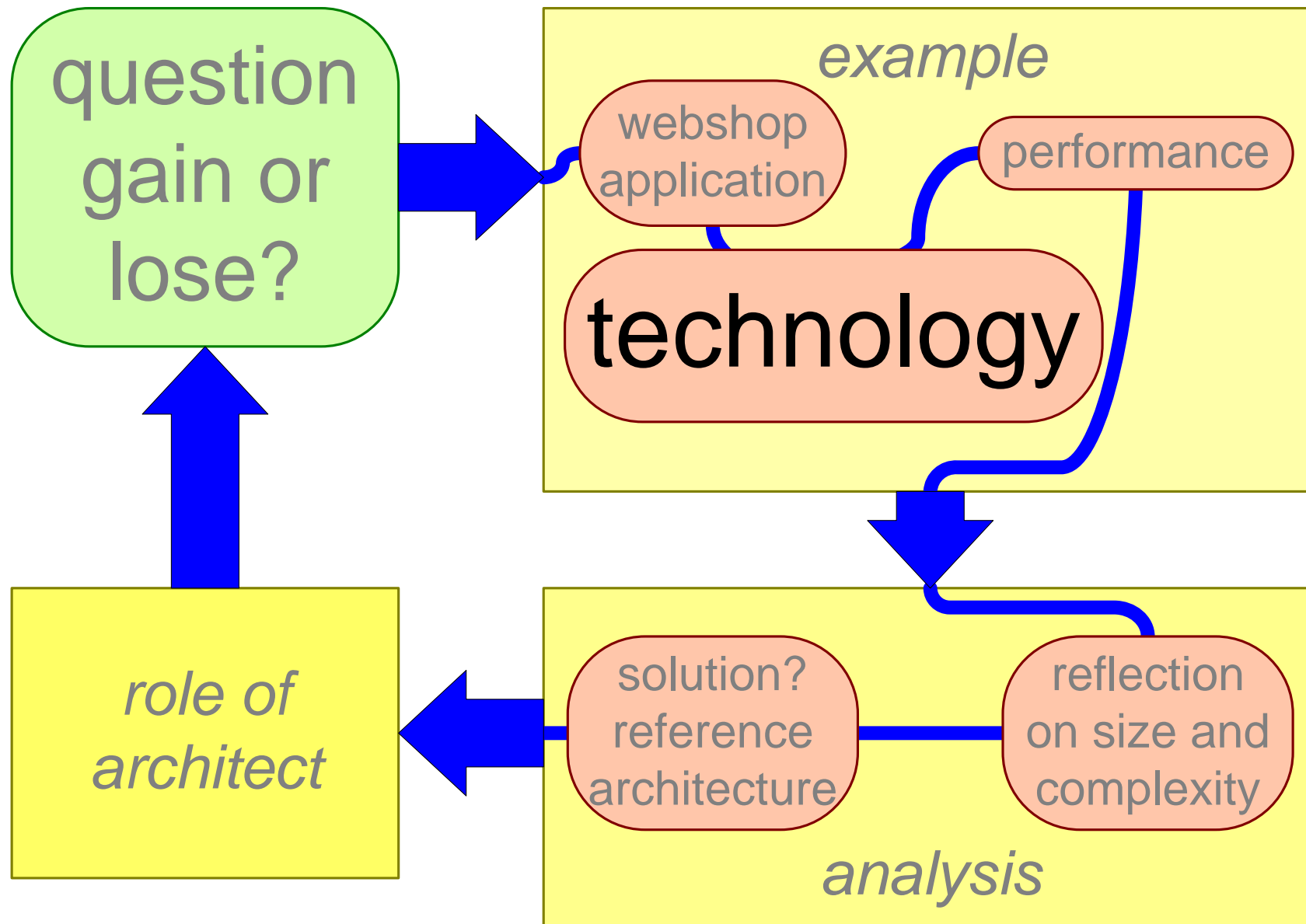
source: http://en.wikipedia.org/wiki/Long_tail

new books per year

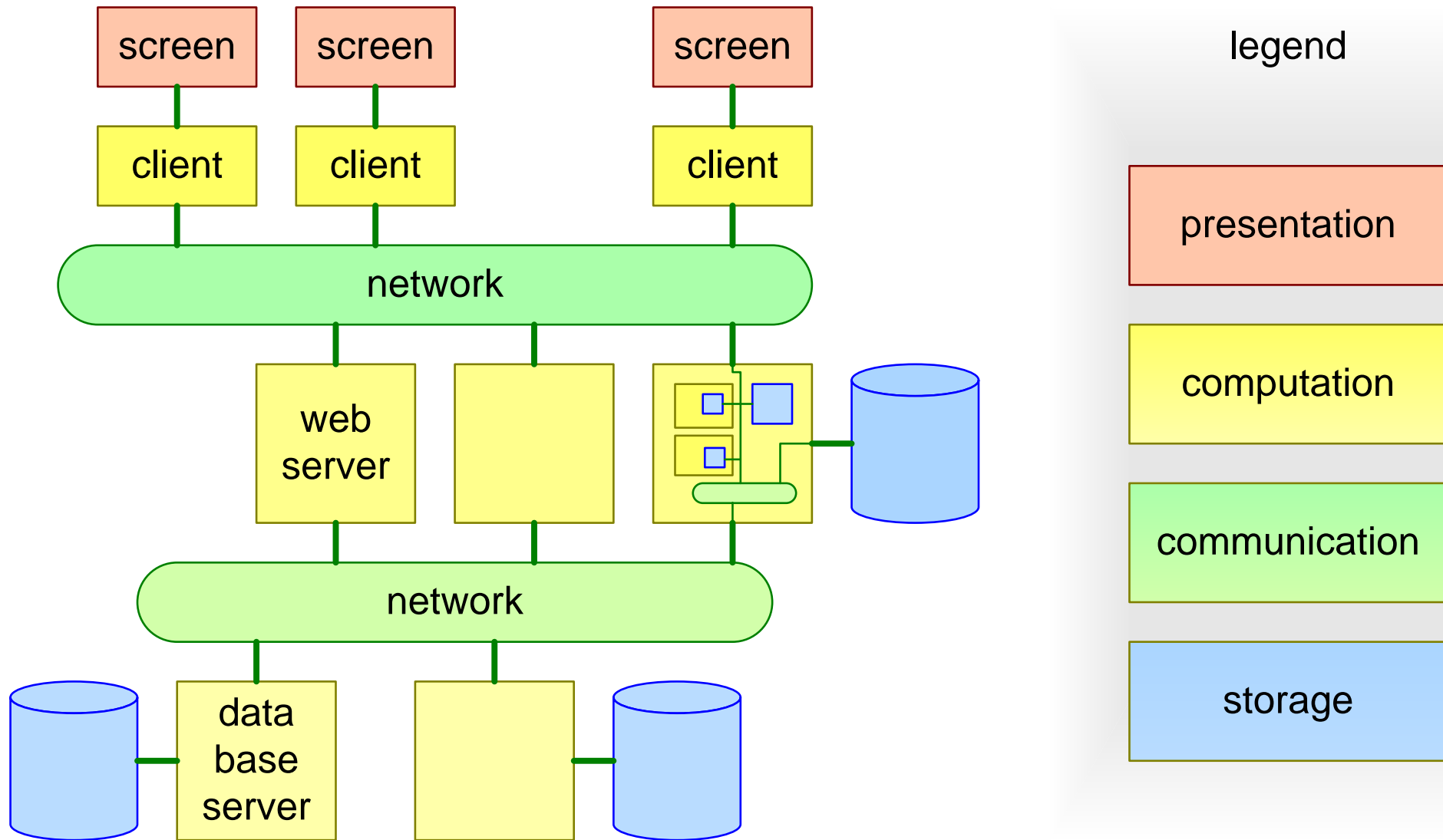
UK (1)	206k (2005)	107k (1996)
USA(2)	172k (2005)	68k (1996)
China(3)		101k (1994)
India(21)		12k (1996)



source: http://en.wikipedia.org/wiki/Books_published_per_country_per_year



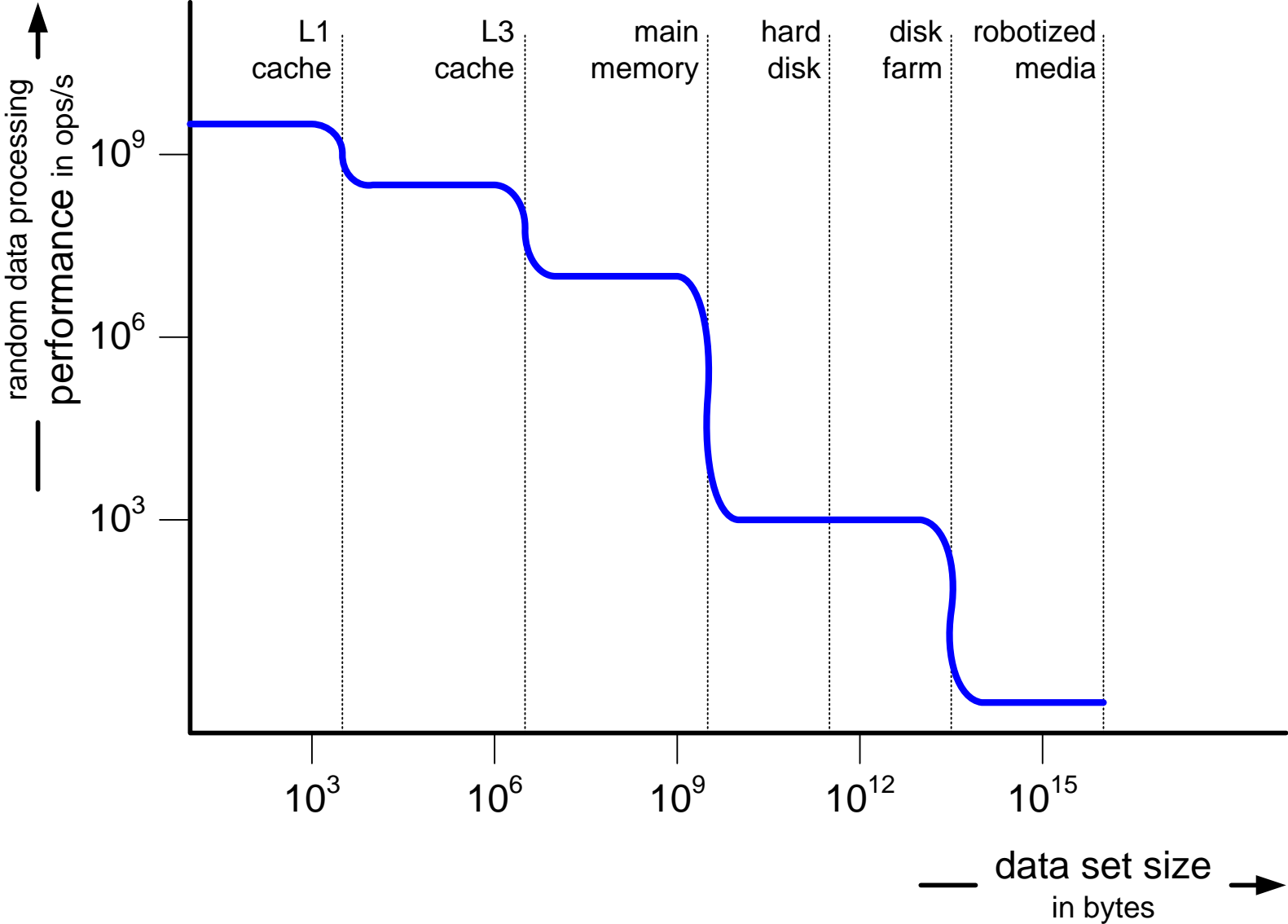
Typical Block Diagram and Typical Resources



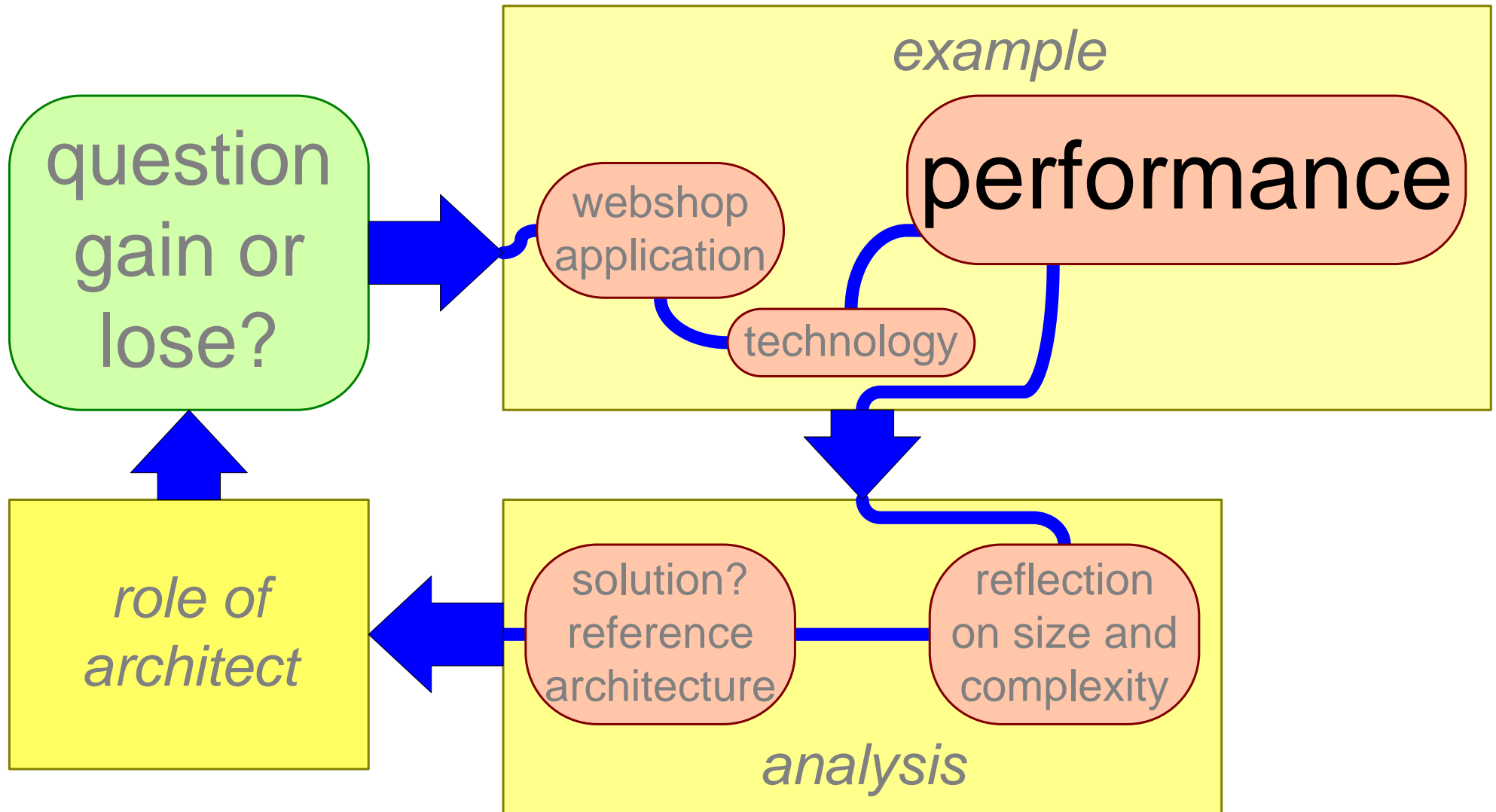
Hierarchy of Storage Technology Figures of Merit

		latency	capacity
processor cache	<i>L1 cache</i>	sub ns	n kB
	<i>L2 cache</i>		
	<i>L3 cache</i>	ns	n MB
fast volatile	<i>main memory</i>	tens ns	n GB
persistent	<i>disks</i>		n*100 GB
	<i>disk arrays</i>	ms	
	<i>disk farms</i>		n*10 TB
archival	<i>robotized optical media tape</i>	>s	n PB

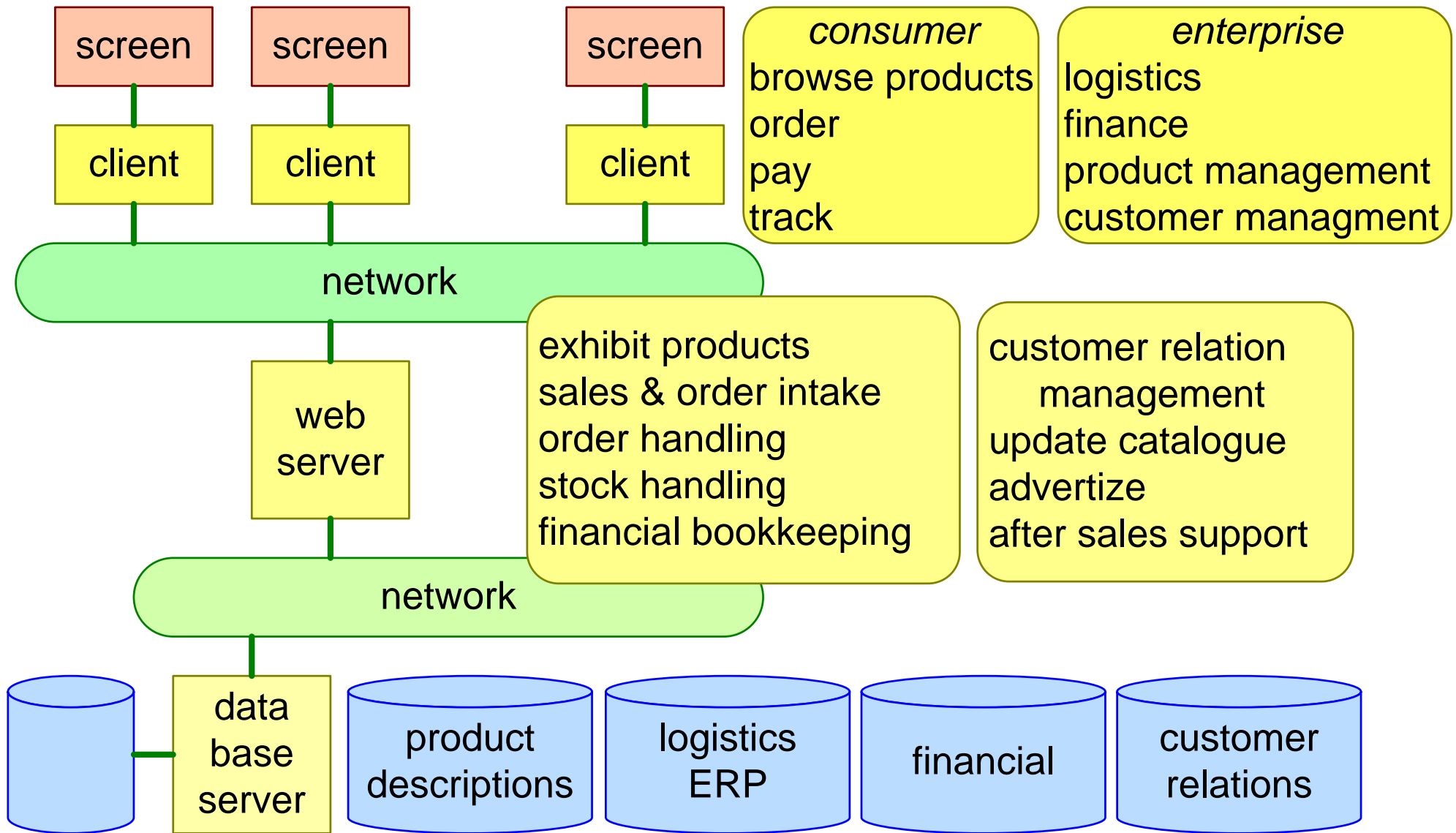
Performance as Function of Data Set Size



		latency	frequency	distance
on chip	<i>connection</i>	sub ns	n GHz	n mm
	<i>network</i>	n ns	n GHz	n mm
PCB level		tens ns	n 100MHz	n cm
Serial I/O		n ms	n 100MHz	n m
network	<i>LAN</i>	n ms	100MHz	n km
	<i>WAN</i>	n 10ms	n GHz	global



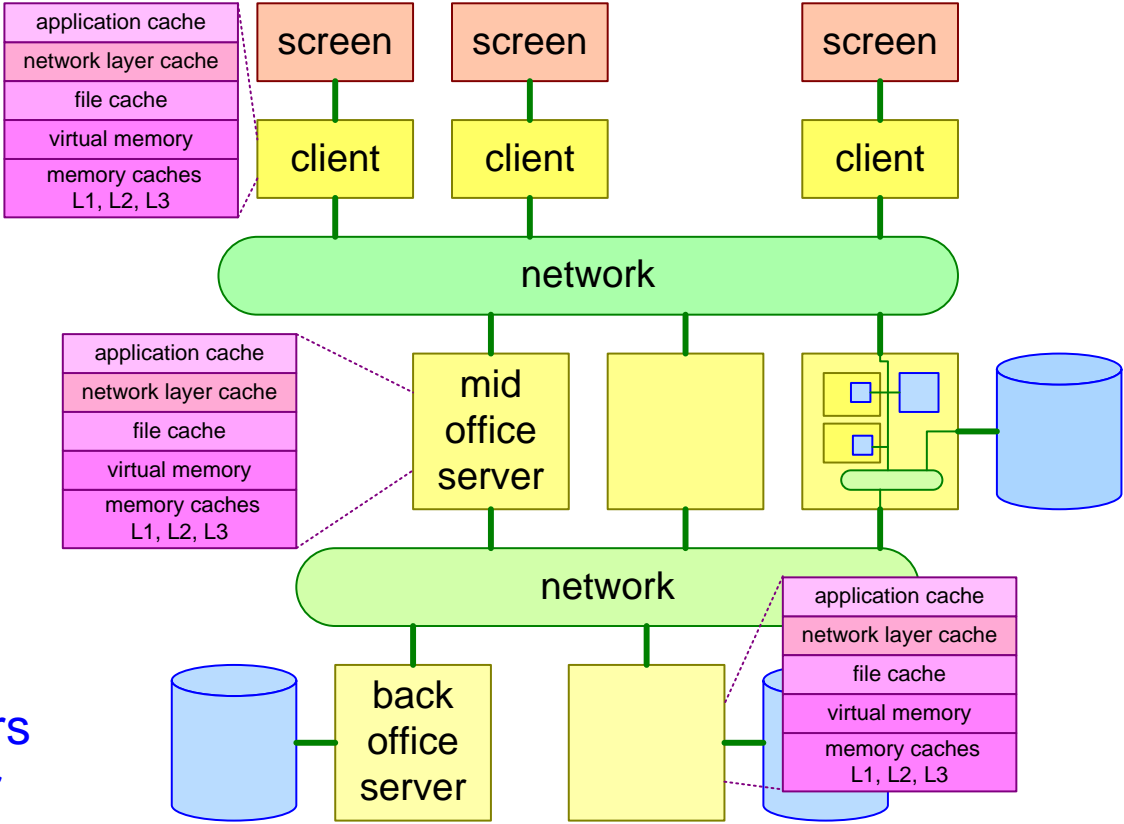
Example Web Shop



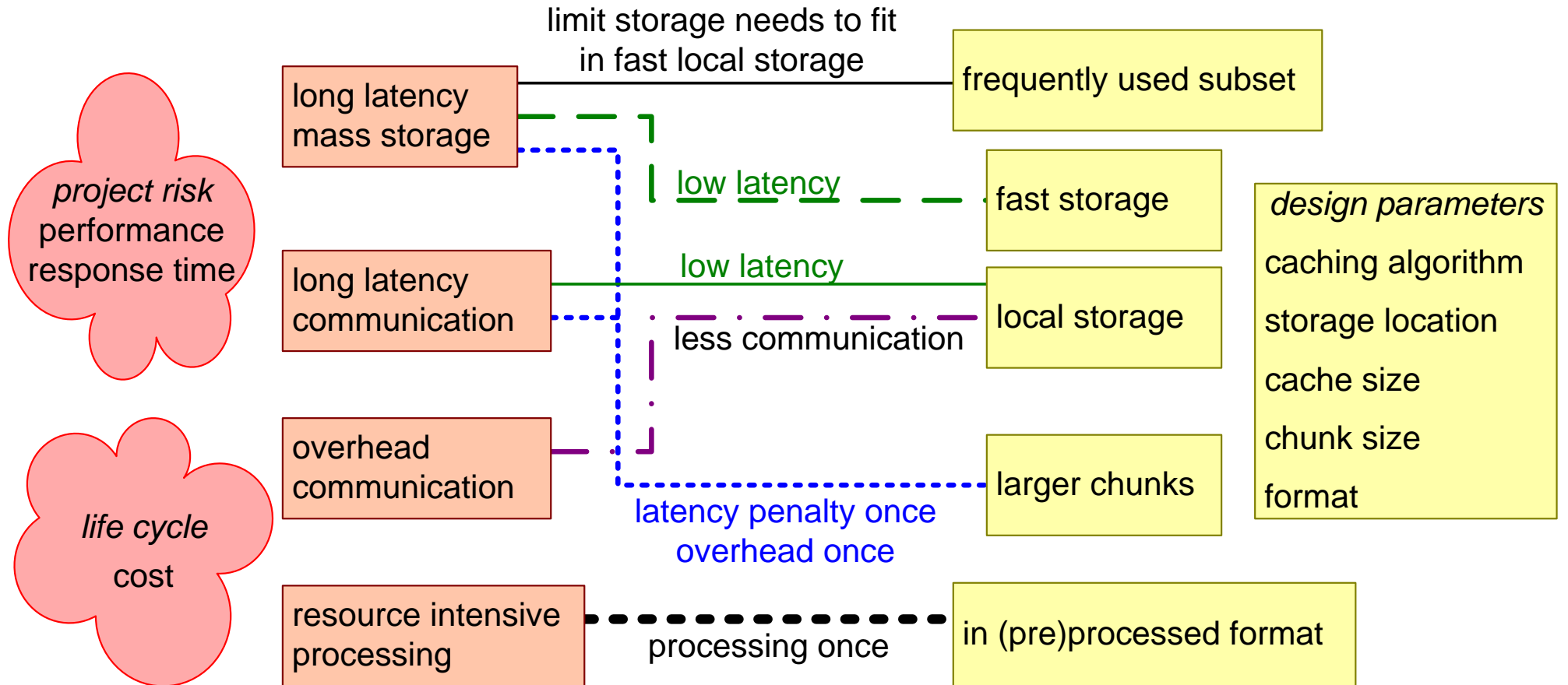
Multiple Layers of Caching

	cache miss penalty	cache hit performance
application cache	1 s	10 ms
network layer cache	100 ms	1 ms
file cache	10 ms	10 μs
virtual memory	1 ms	100 ns
memory caches L1, L2, L3	100 ns	1 ns

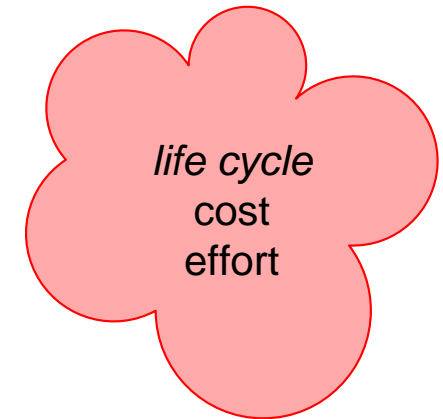
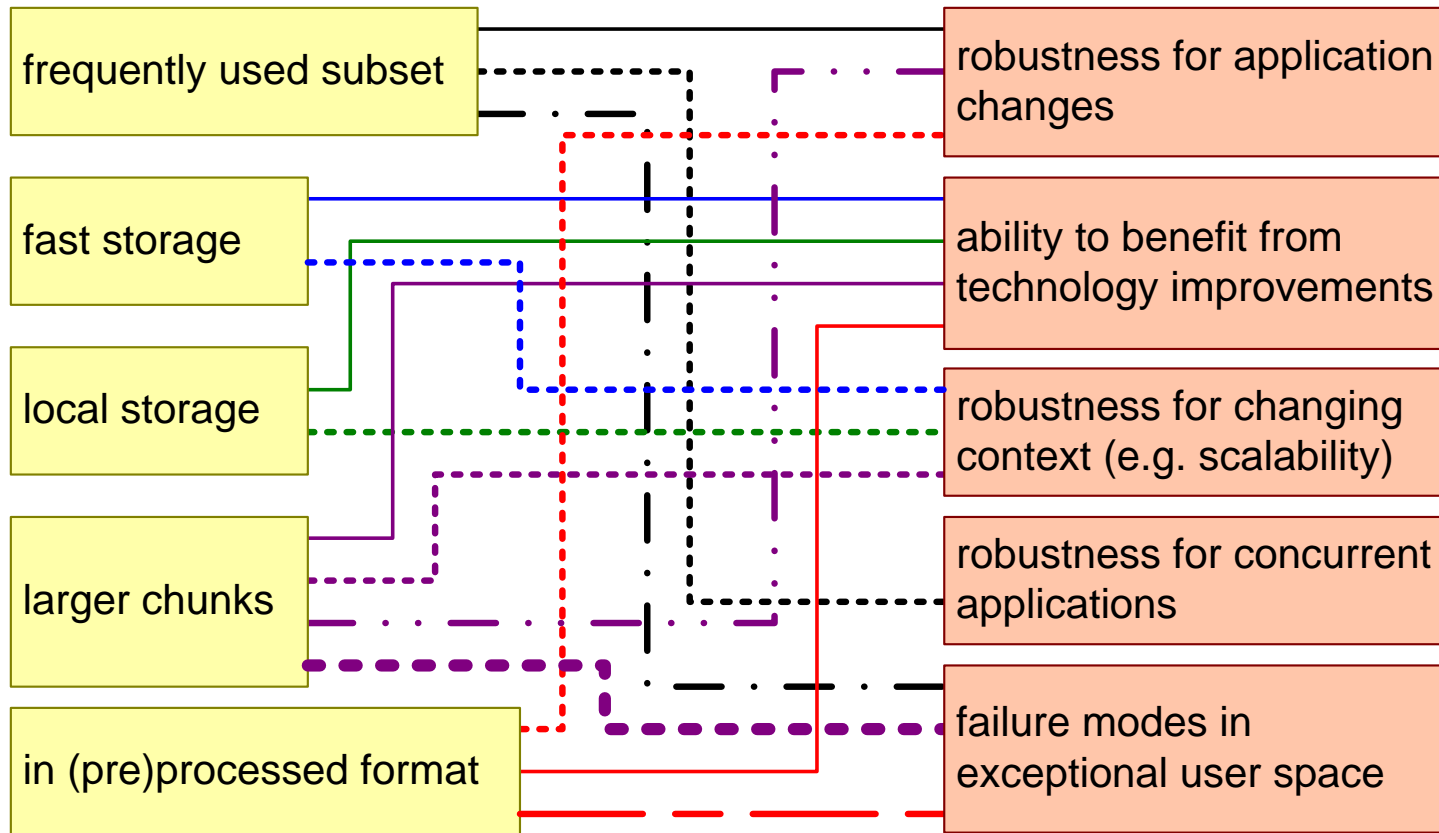
↔
typical cache 2 orders of magnitude faster



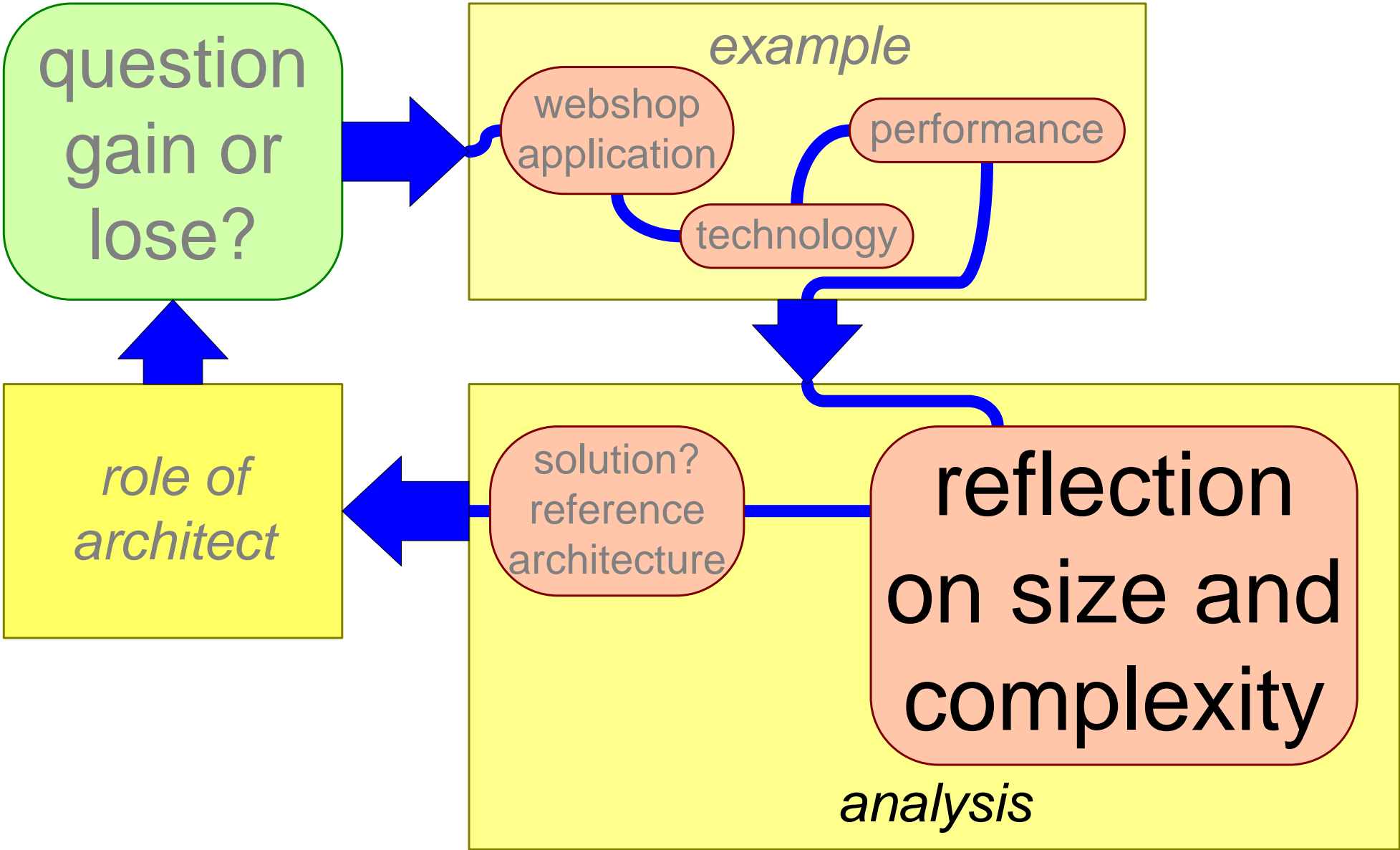
Why Caching?



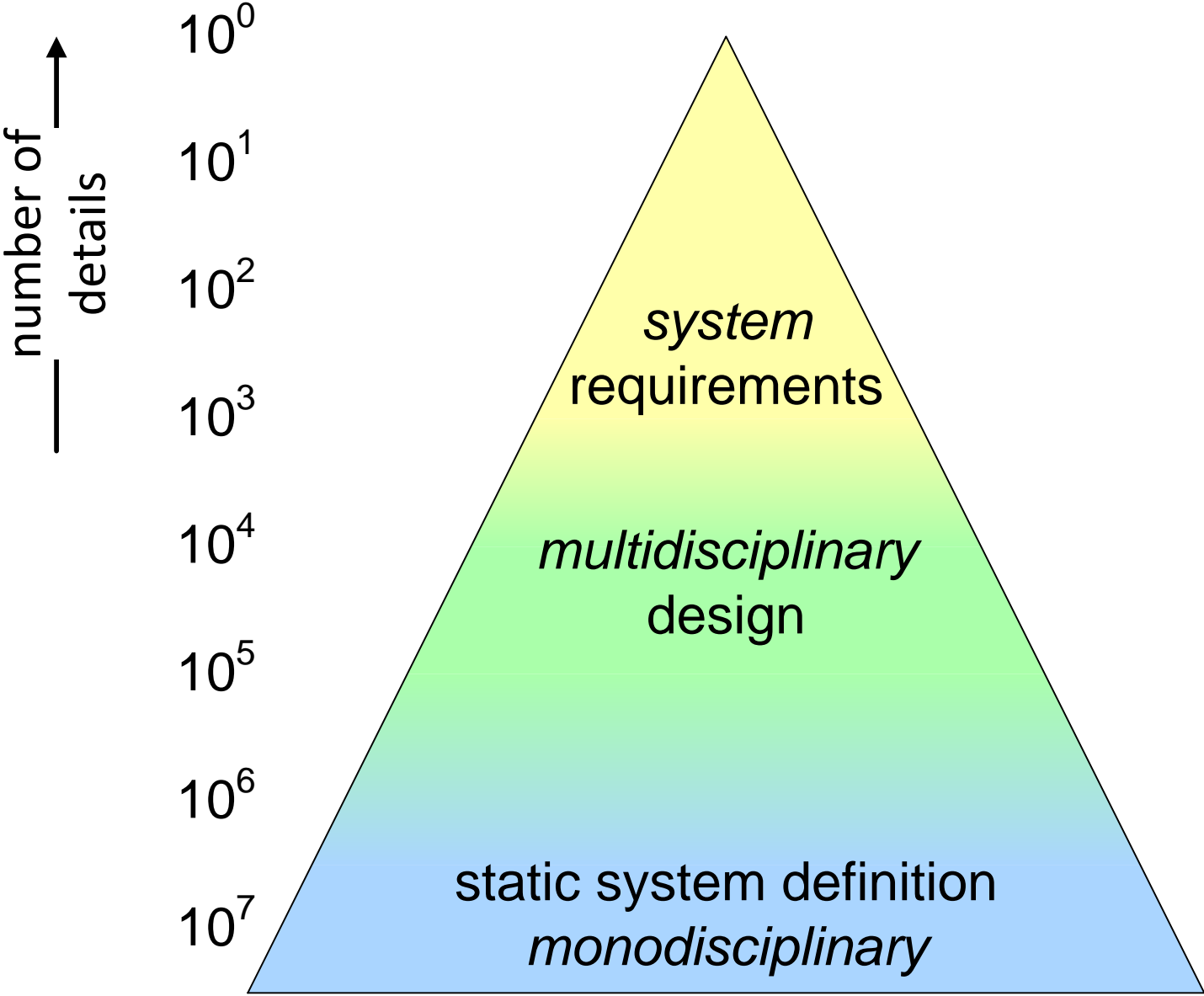
Risks of Caching



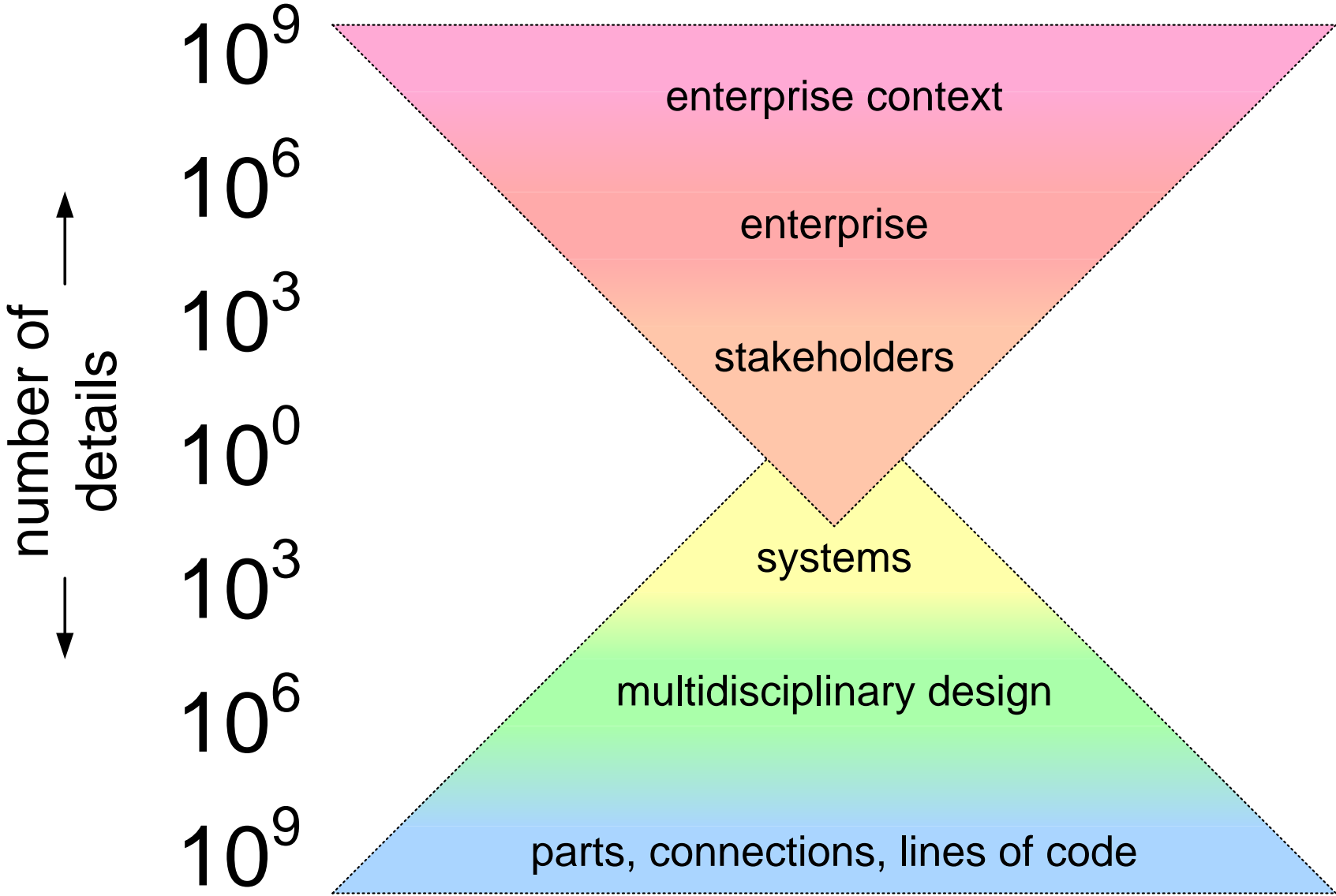
Size and Complexity



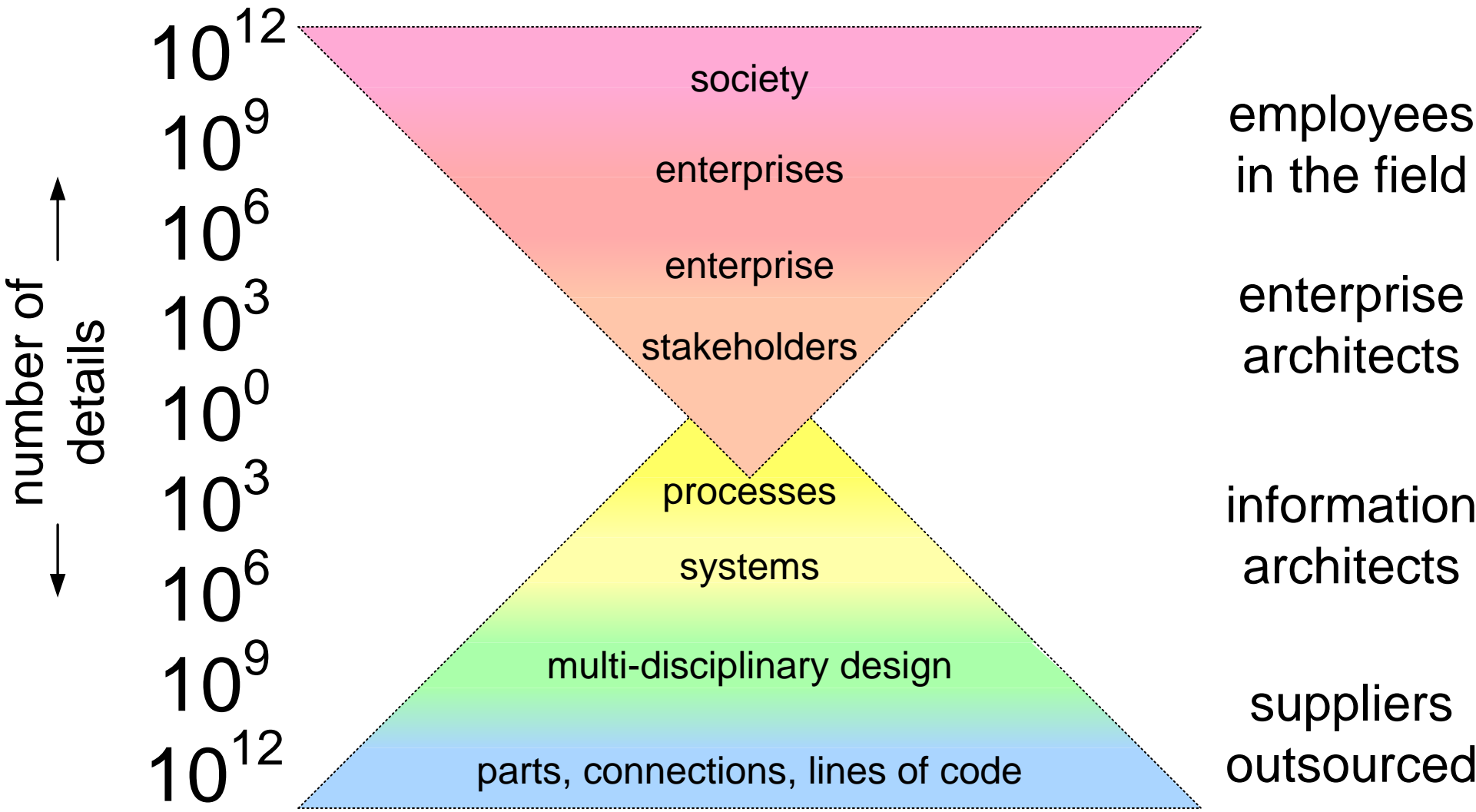
Level of Abstraction Single System



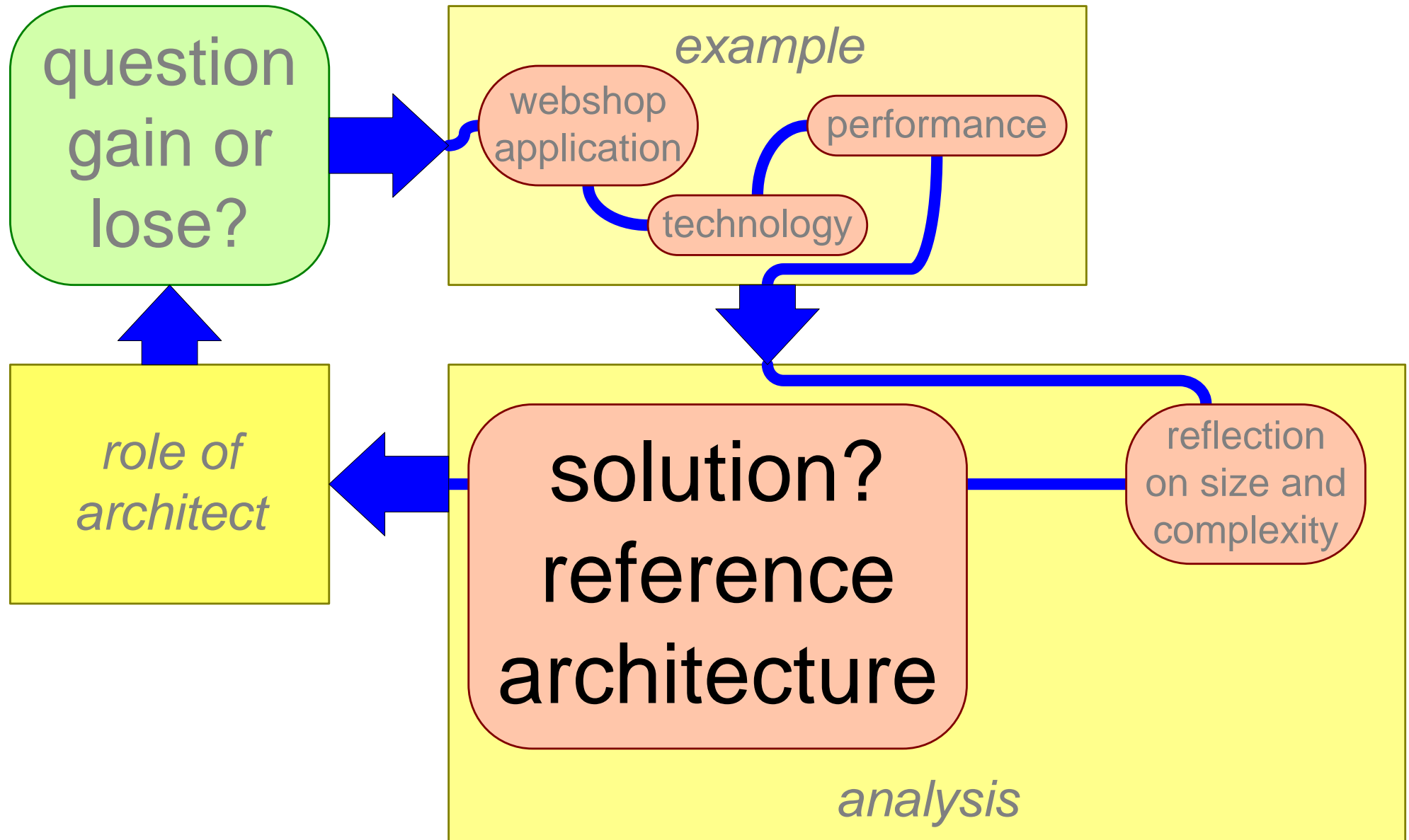
Product Family in Context



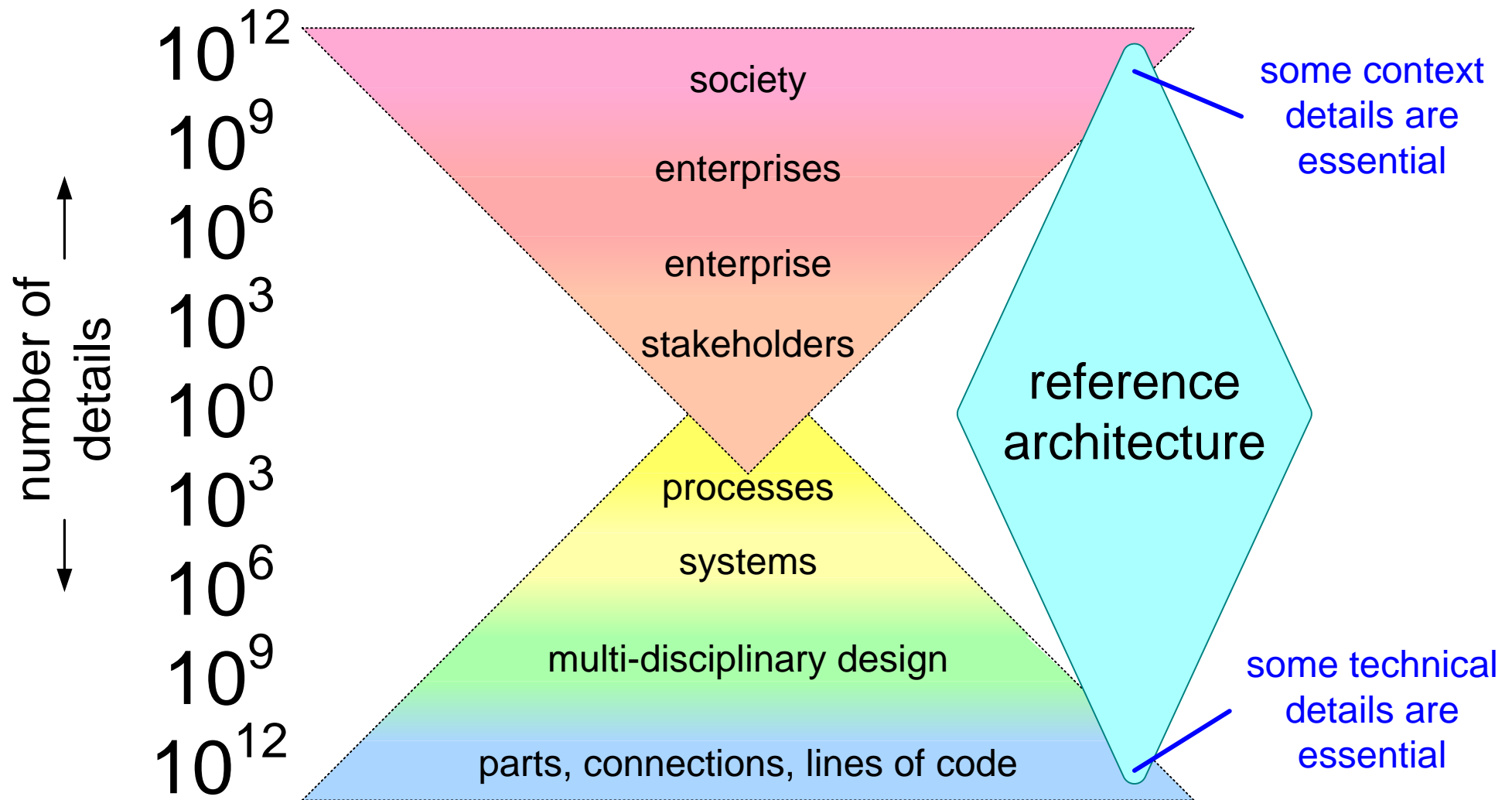
Number of Details in Today's Services



Reference Architecture



Reference Architecture as Solution?



1.1 One of several prerequisites for architecture creative synthesis is the definition of **5-7 specific key drivers** that are critical for success, along with the rationale behind the selection of these items

2.1. The essence of a system can be captured in about **10 models/views**

2.2. A **diversity** of architecture descriptions and models is needed: languages, schemata and the degree of formalism.

2.3. The level of **formality** increases as we move closer to the implementation level.

from <http://www.architectingforum.org/bestpractices.shtml>

Possible useful visualizations



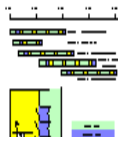
COVmotorwayManagementKeyDrivers



LWAValueChain



COVsuppliers



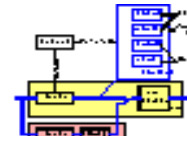
AVdynamicsURF



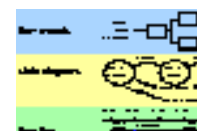
AVstakeholders



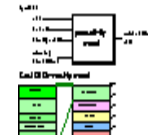
AVcontextMotorwayManagement



AVsimpleTVmodel



AVdynamicModels



AVcostBenefitModels



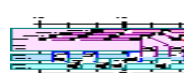
SHTexampleStoryLayout



ETexampleTimeShiftingWhatIf



MICAftypicalCase



MICAftypicalTiming



MICAfclinicalInfoFlow



MICAfrequestFlow



MICAffinancialContext



MICAfsystemLayers



MICAfreferenceModel



MICAfmarketSegmentation



MICAfinformationLayers



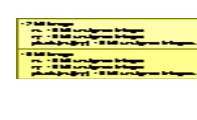
FVcommercialTree



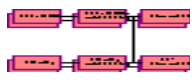
FVfeatureMatrix



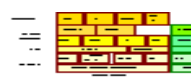
FVinformationModel



FVdatamodel



CVfunctionalDecomposition



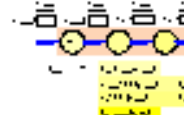
CVconstructionDecomposition



CVinformationModel



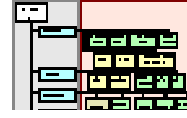
CVprocessDecomposition



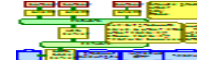
CVreconstructionPerformanceModel



CVstartUp



CVworkBreakdown



MAFTexampleWebShop



CVintegrationPlan



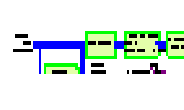
RVperformanceCostEffort



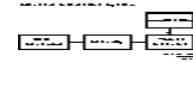
RVmemoryBudgetTable



ASMLoverlayBudget



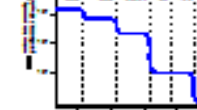
MICVpresentationPipeline



FFTstandardInteractiveSystemAnnotated



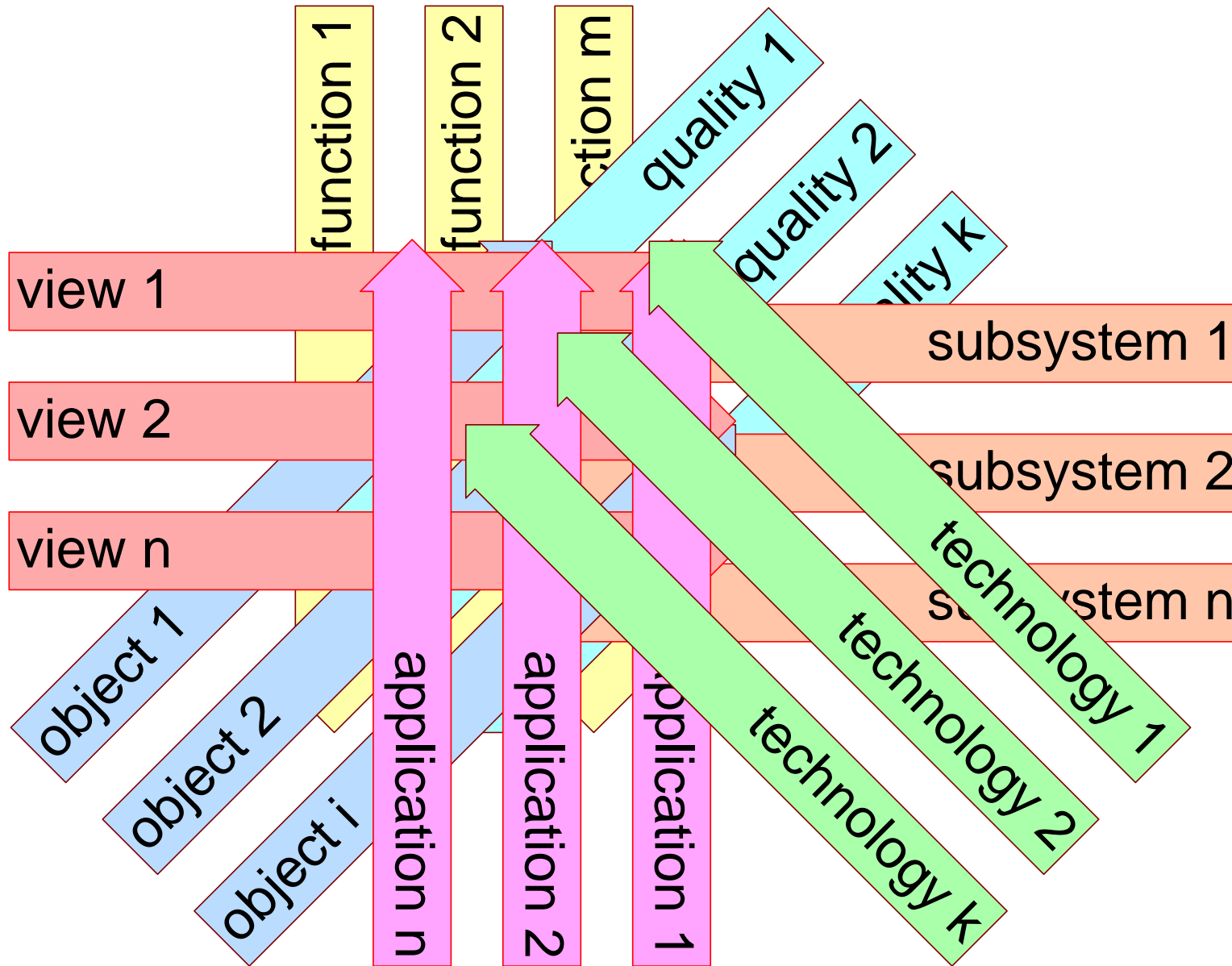
EBMImemoryTimingARM



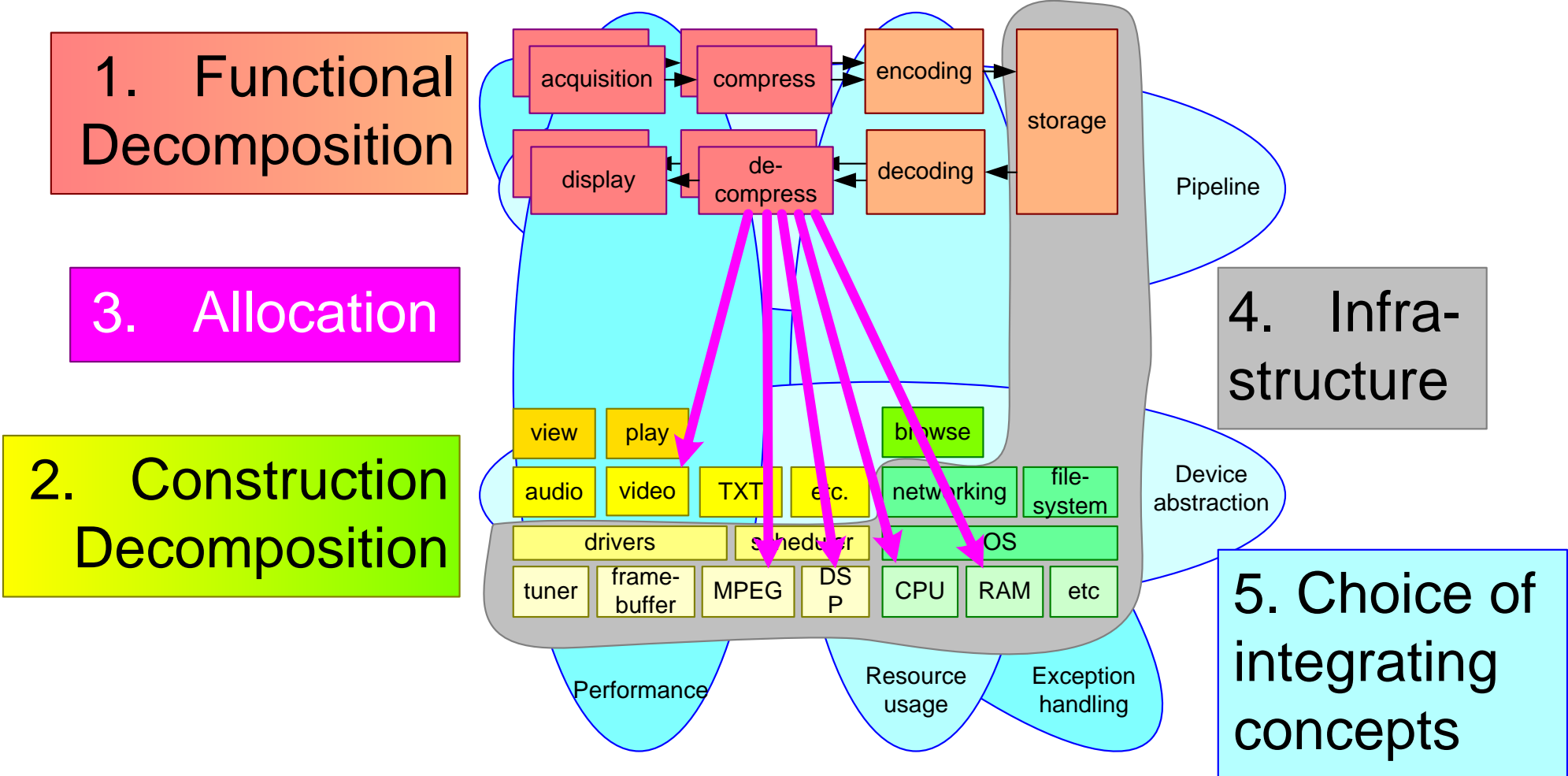
MAFTstoragePerformance

actual figures and references to their use at <http://www.gaudisite.nl/figures/<name>.html>

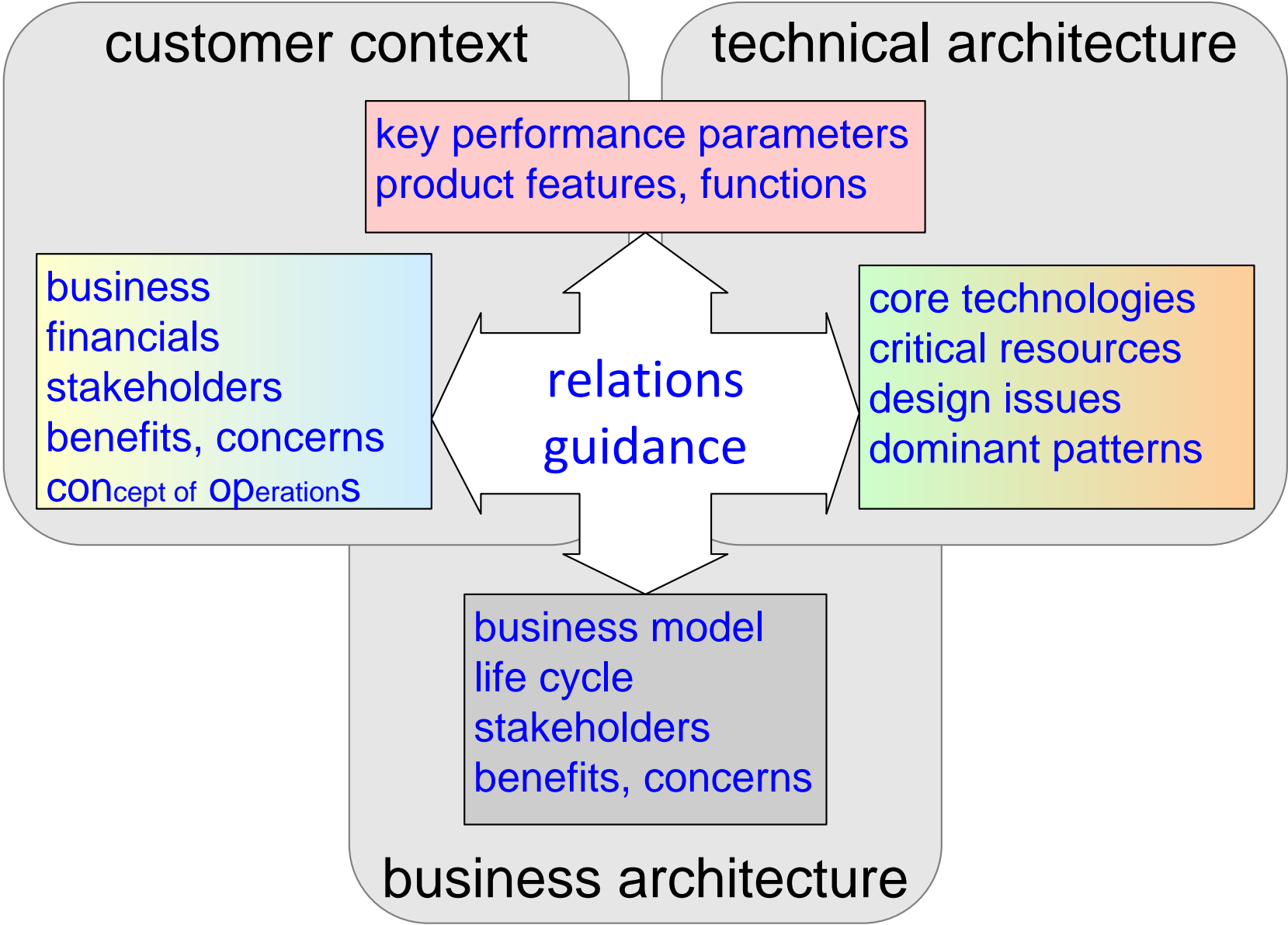
Ideal Structure does not exist



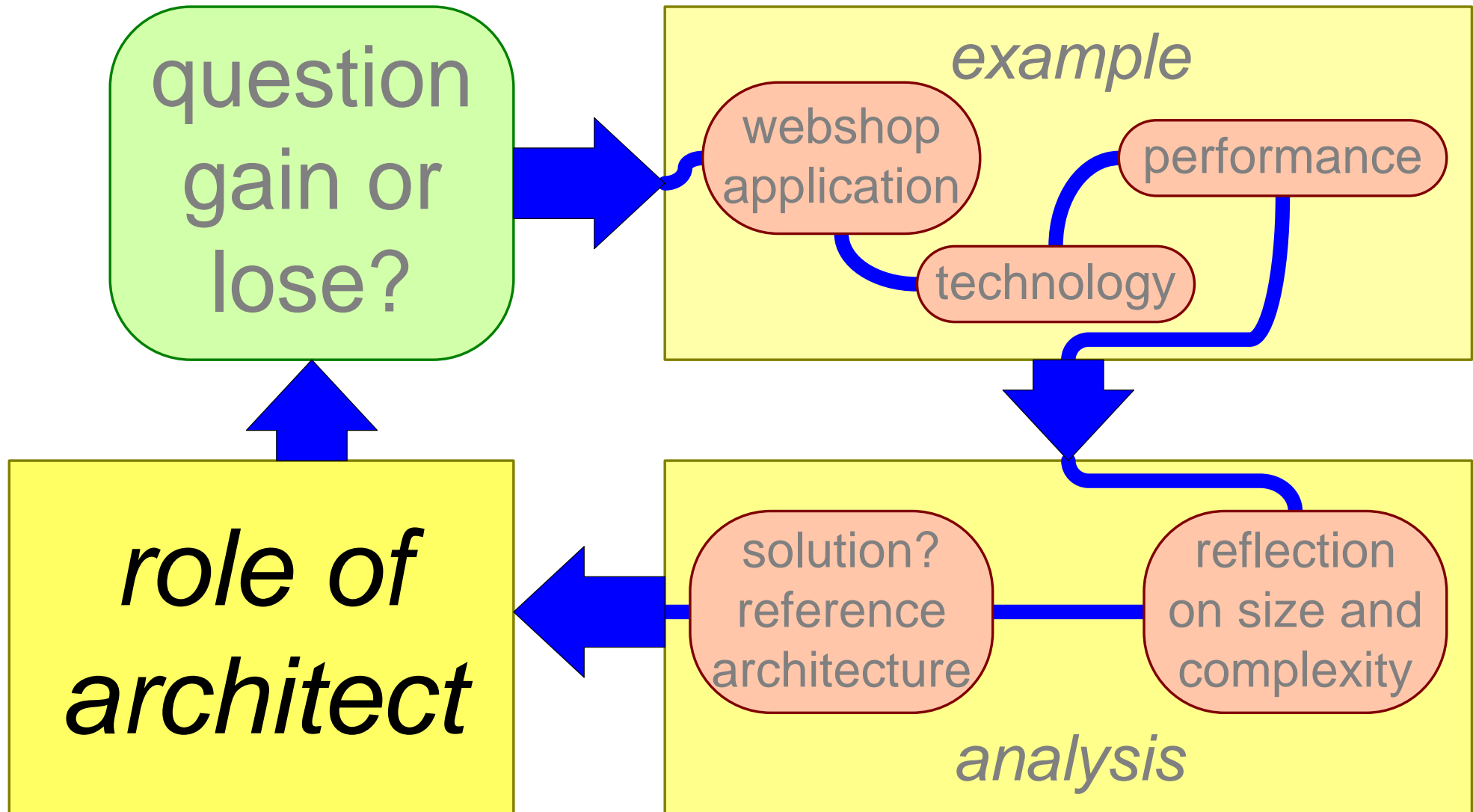
Synthesis, Integration, Relation oriented



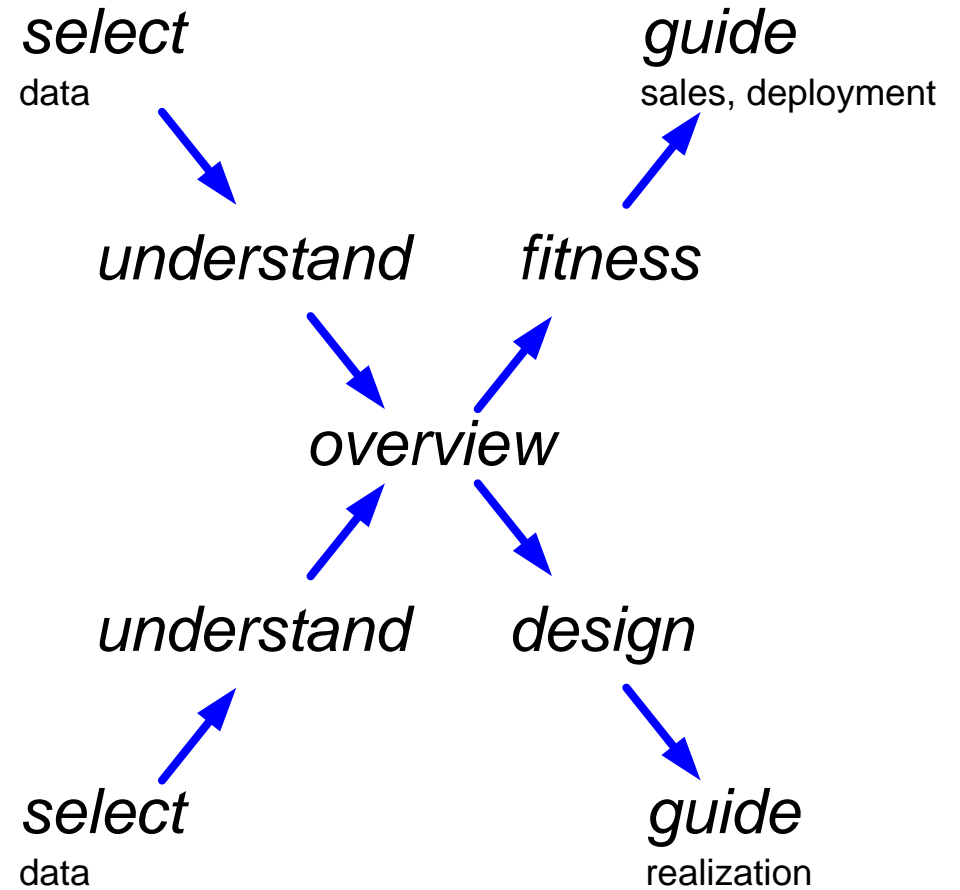
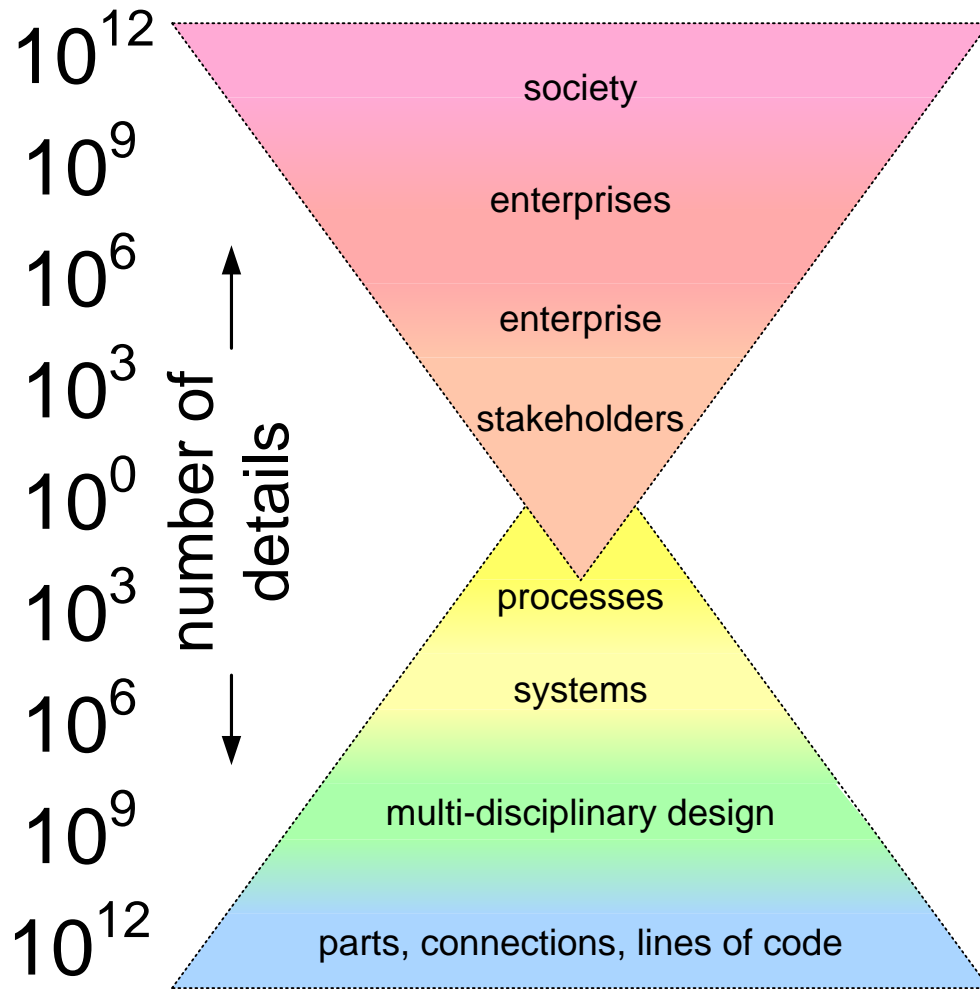
Checklist for RA content



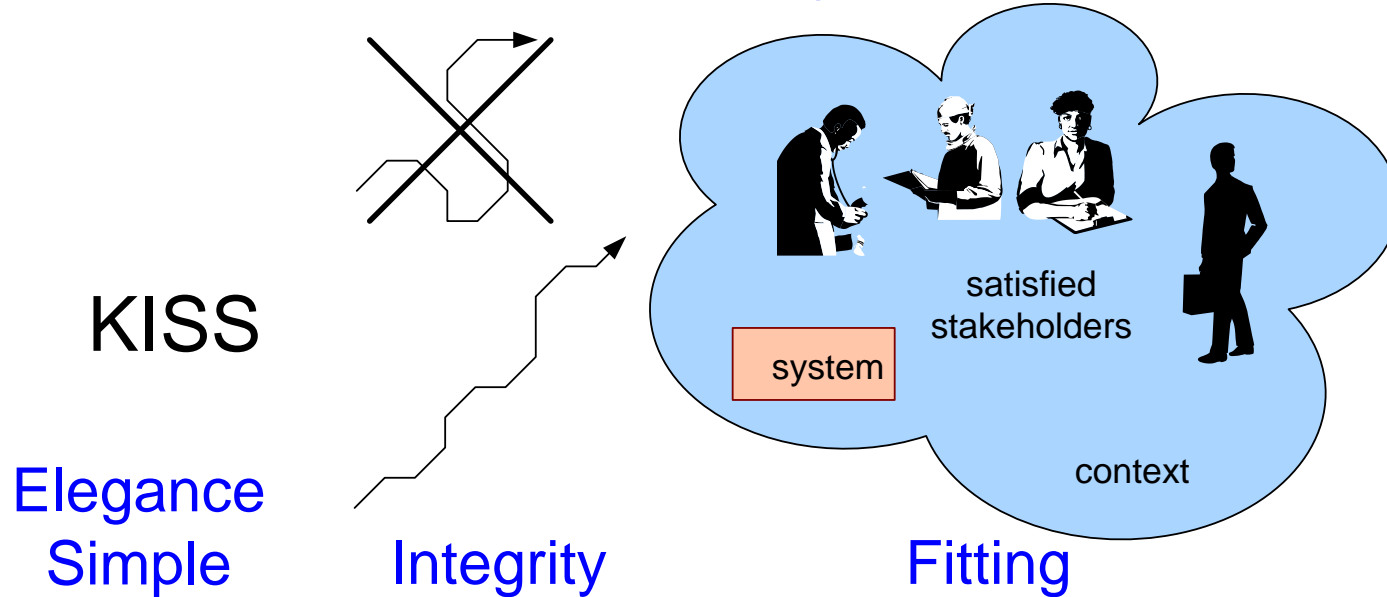
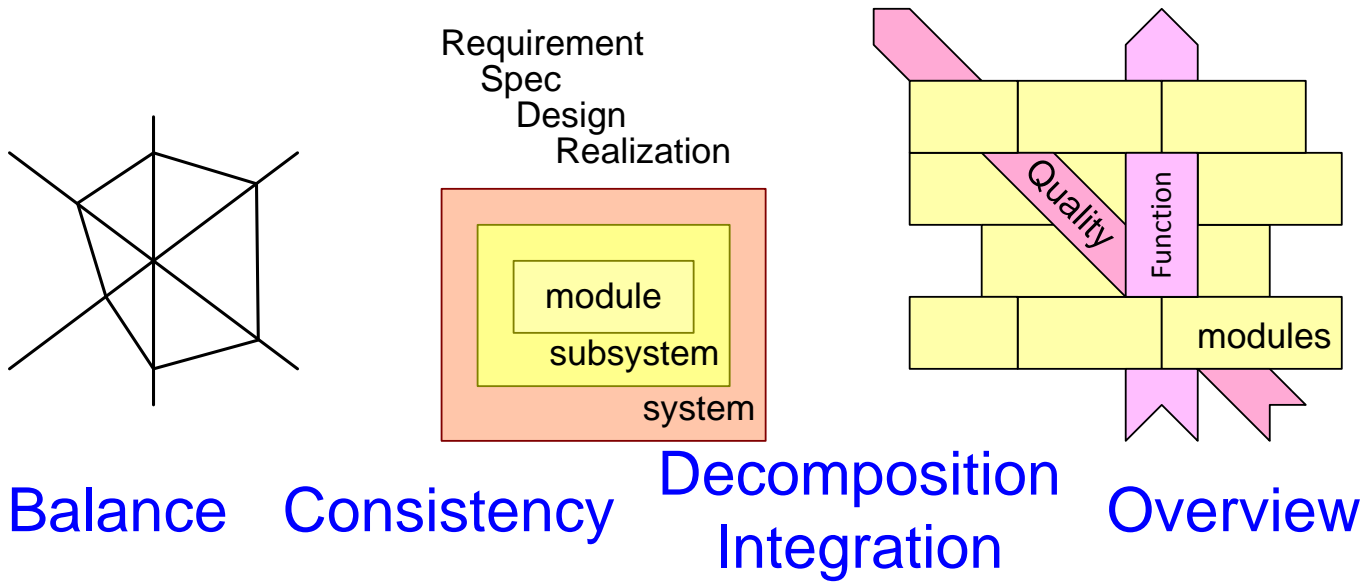
Role of Architect



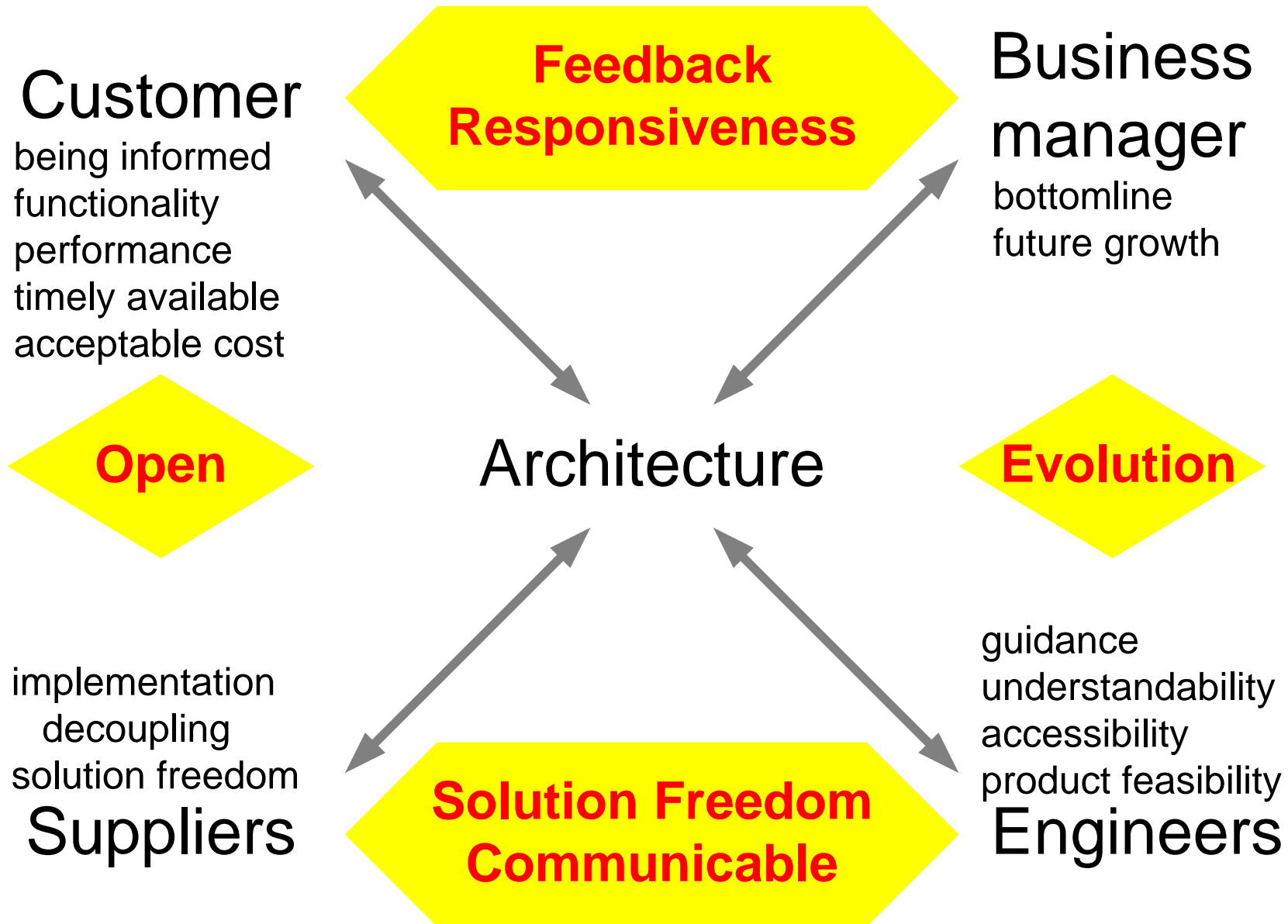
Tasks of Architect



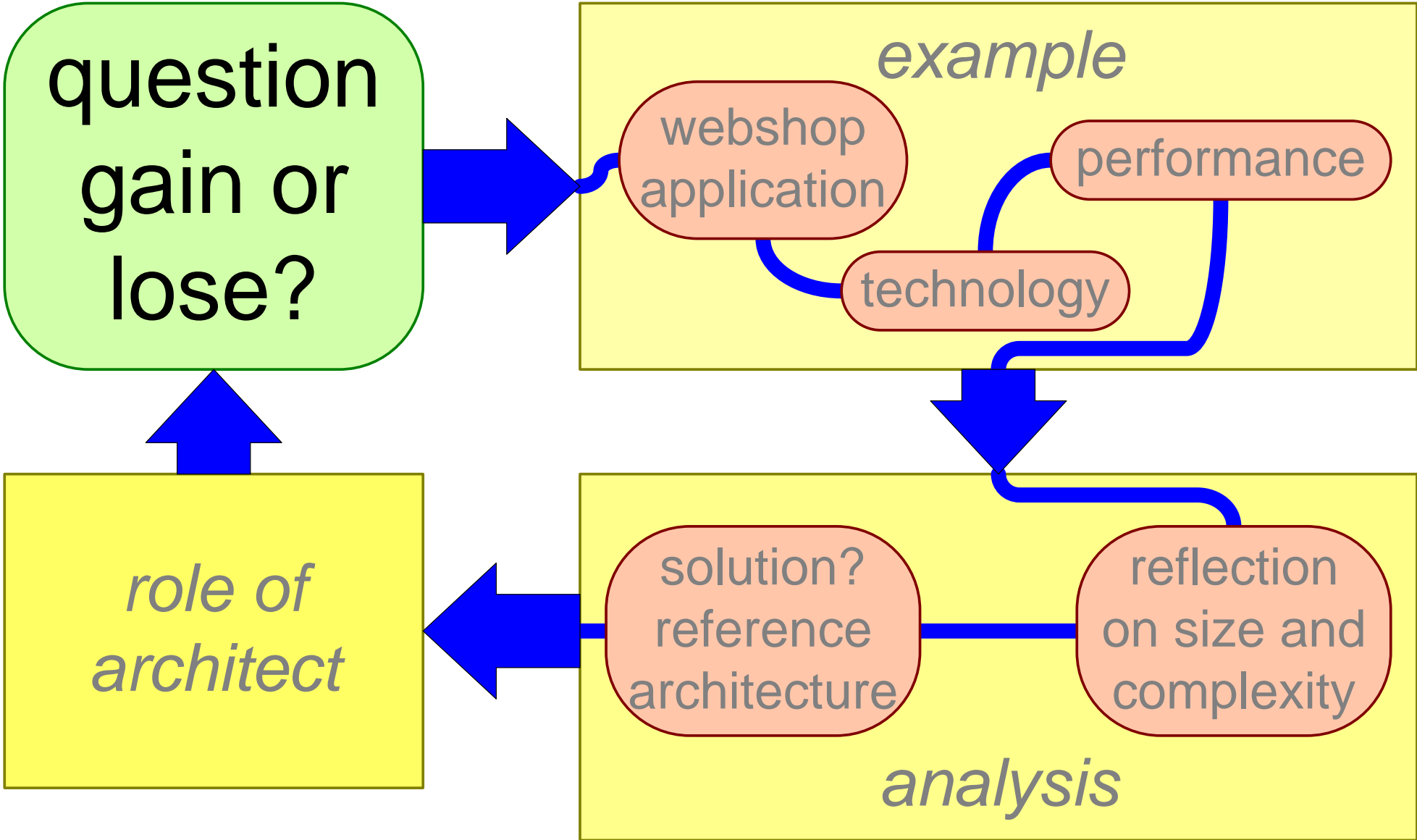
Responsibilities



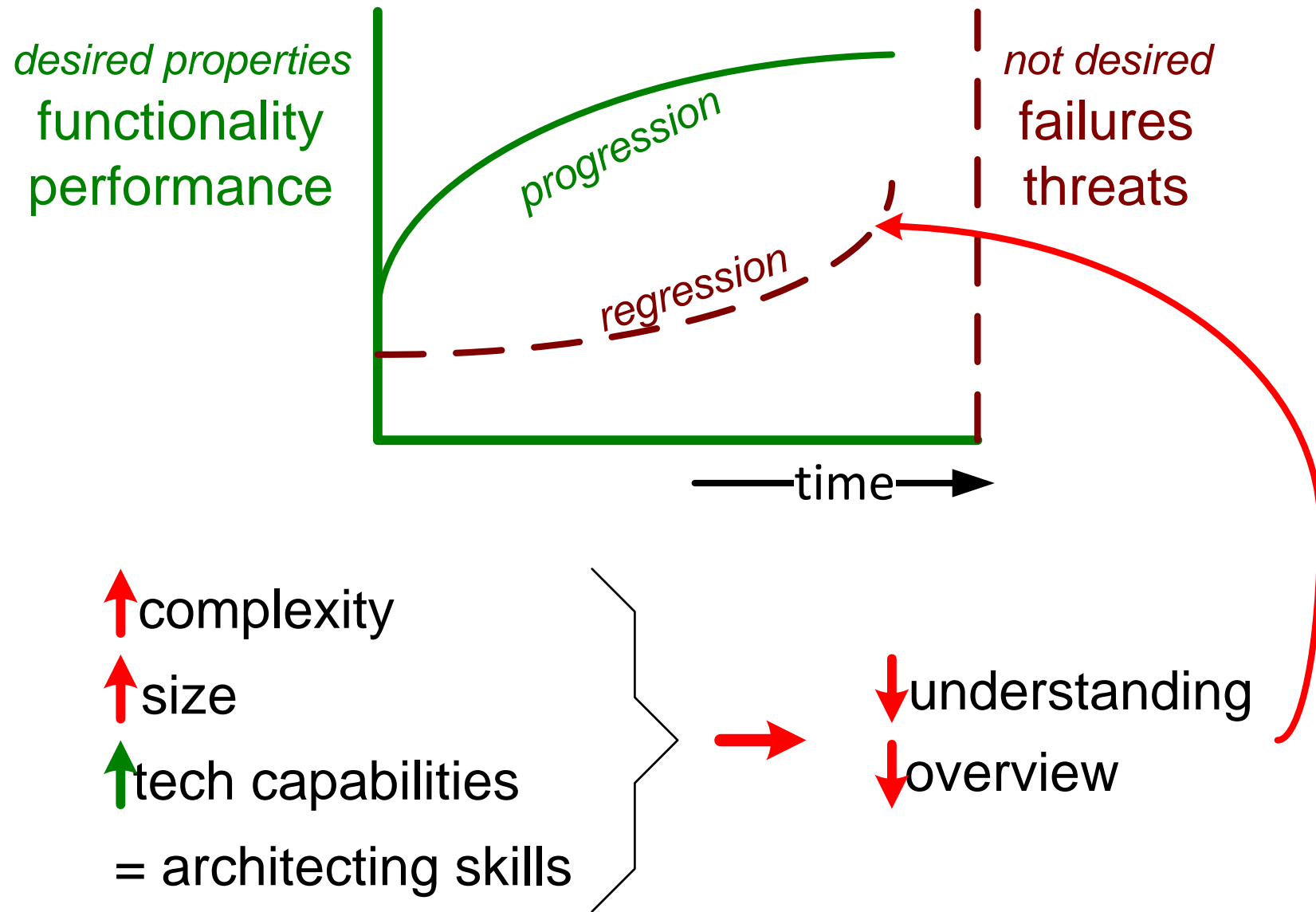
Stakeholders



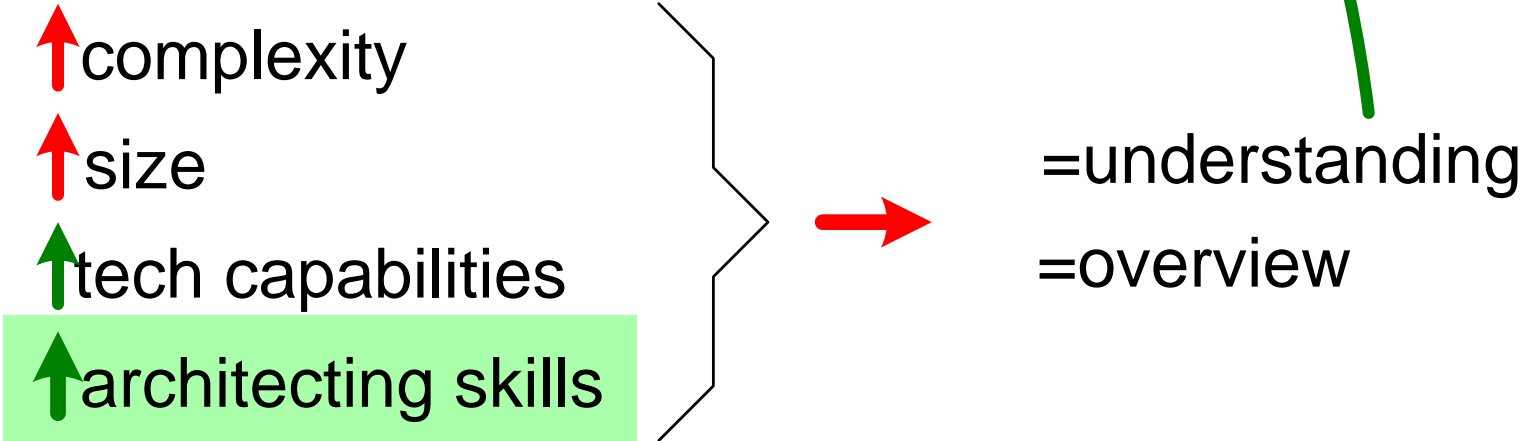
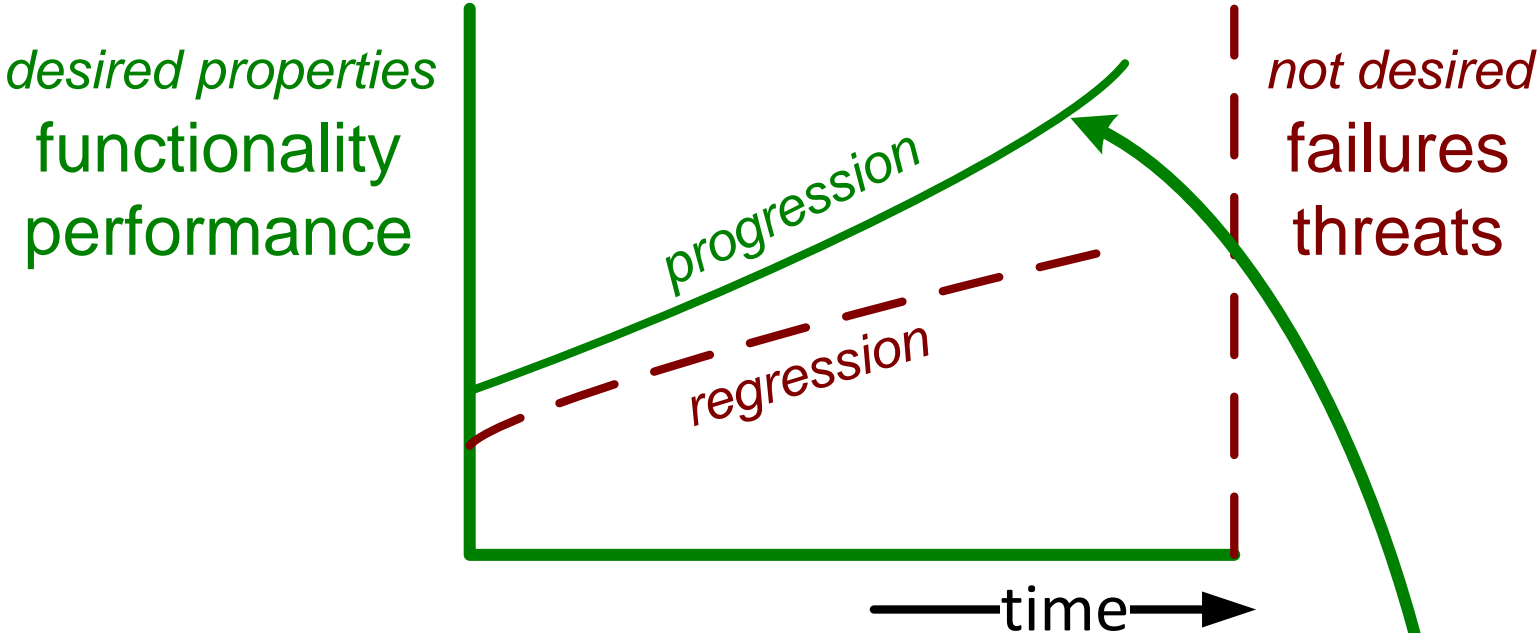
Gain or Lose?



Loss Scenario

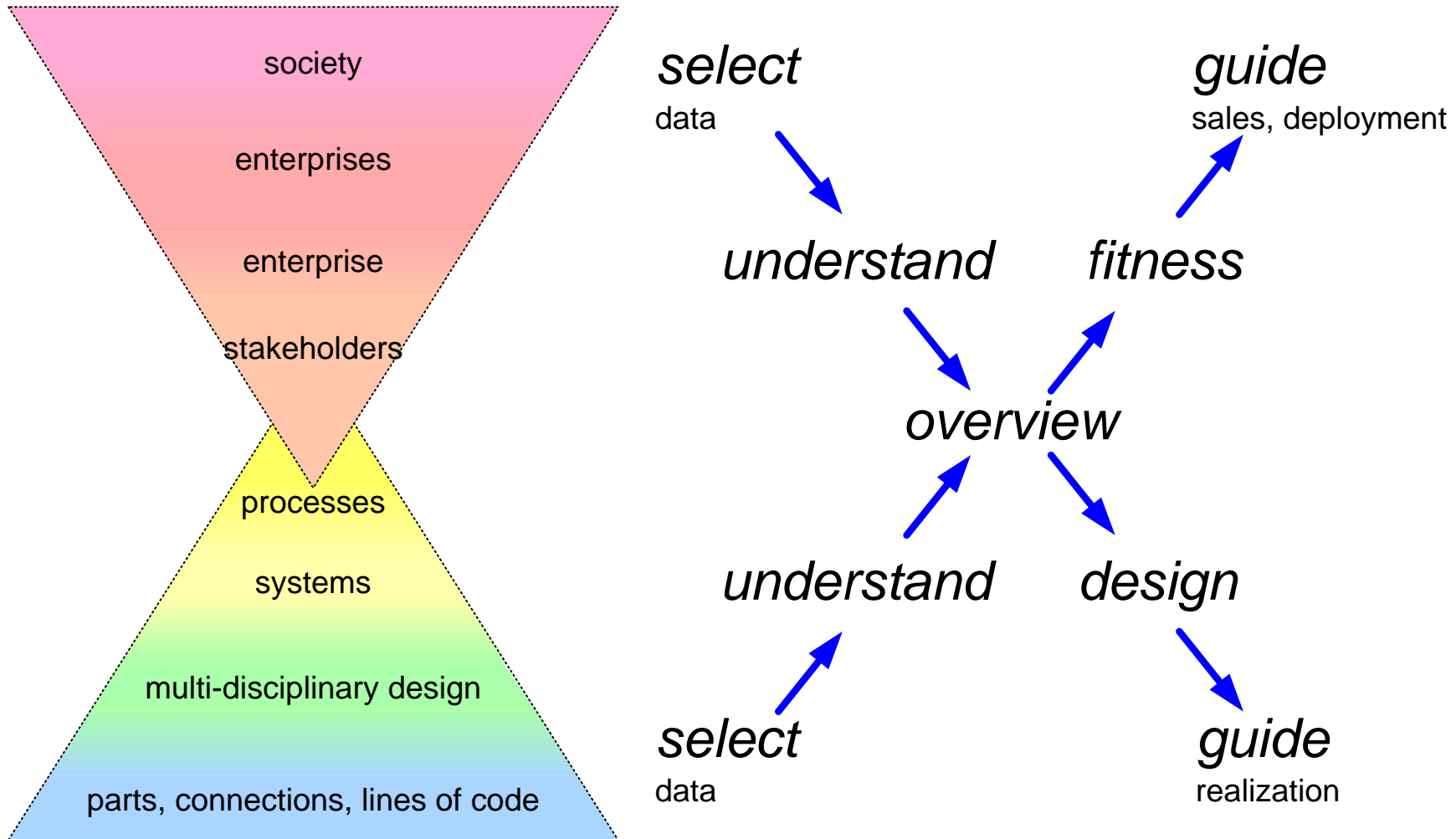


Gain Scenario



Conclusion

We need to improve architecting skills to gain.



Read More at the Gaudisite

<http://www.gaudisite.nl/>

Reference Architecture Primer

<http://www.gaudisite.nl/ReferenceArchitecturePrimerPaper.pdf>

Webshop case is part of System Modeling and Analysis

<http://www.gaudisite.nl/SystemModelingAndAnalysisBook.pdf>

All about Architecting: System Architecting

<http://www.gaudisite.nl/SystemArchitectureBook.pdf>